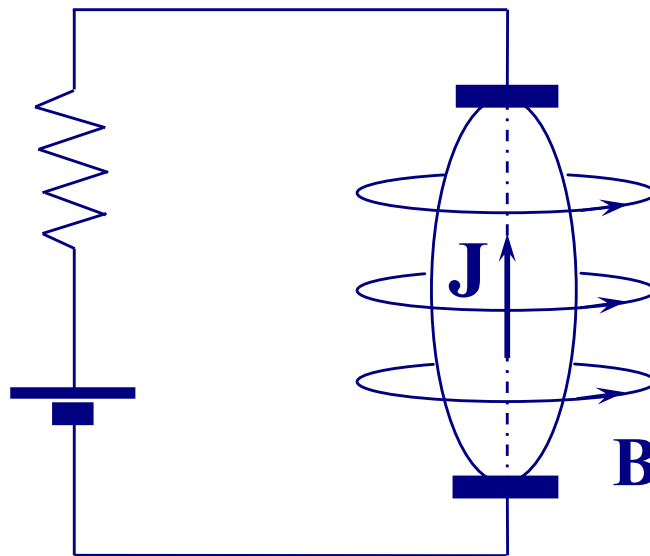
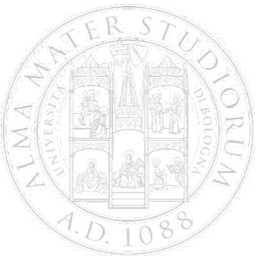




## 2. Electric Circuit Theory



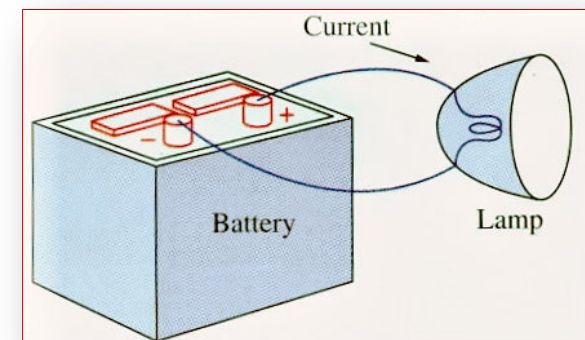


# Electric Circuit Theory



Electric circuit theory and Electromagnetic theory are the two fundamental theories upon which all branches of electrical engineering are based. Many of these branches, such as production, transmission and utilization of electric power, electric machines, control, electronics communication, and instrumentation, are based on electric circuit theory.

**In electrical engineering, we want to transfer electric signals or electric power from one point to another. To do this requires an interconnection of electrical devices. A very effective interconnection is the *electric circuit*, which is constituted by electrically interconnected *circuital elements*.**





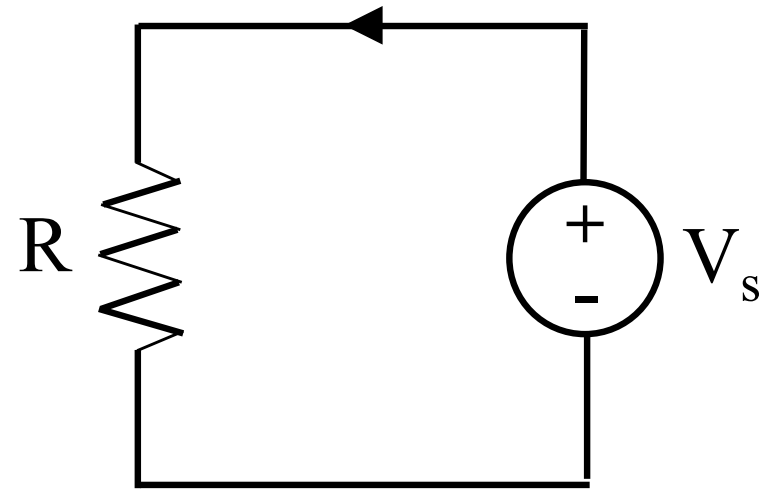
# Electric Circuit Theory

- The term ***electric circuit*** indicates the physical place where electromagnetic phenomena are located. The other meaning of the term ***electric circuit*** regards the mathematical models which describes them.
- Usually the term is utilized to indicate the circuits and the relative models, that satisfy the assumption of ***lumped component models*** (also called ***lumped element models*** or ***lumped parameter models***). This assumption considers all electromagnetic phenomena concentrated and confined inside discrete bodies (named ***lumped components***, or ***circuit components***, or also ***circuit elements***). These elements are electrically connected so that the electric charges can move between them.



# Electric Circuit Theory

- In the figure a representation of a lumped model, or circuit model, made up of a **voltage source** and a **resistor**, is shown. The charges are moved by the voltage of the voltage source and flows from one terminal of the voltage source. They go through the electrical resistance and flow to the other terminal of the voltage source.
- This is possible due to the **electrical connections** joining the two elements so that the electric charges can move between them.





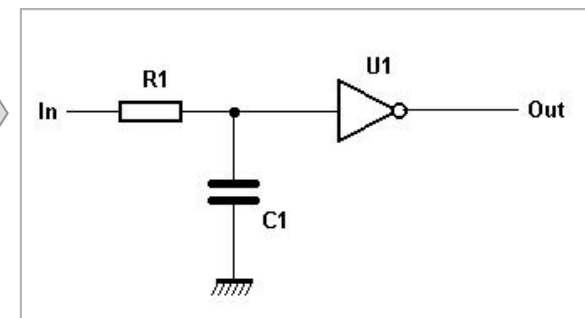
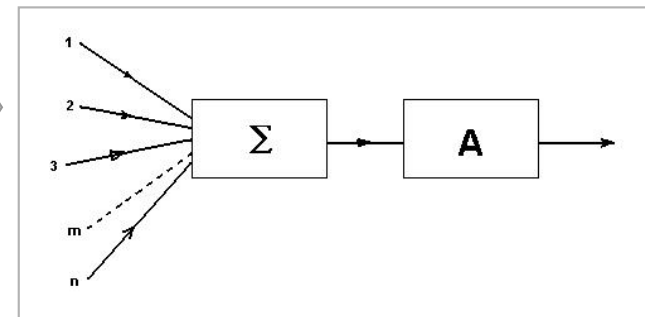
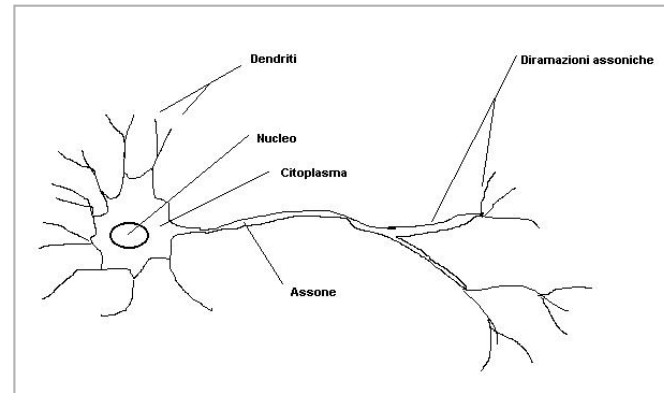
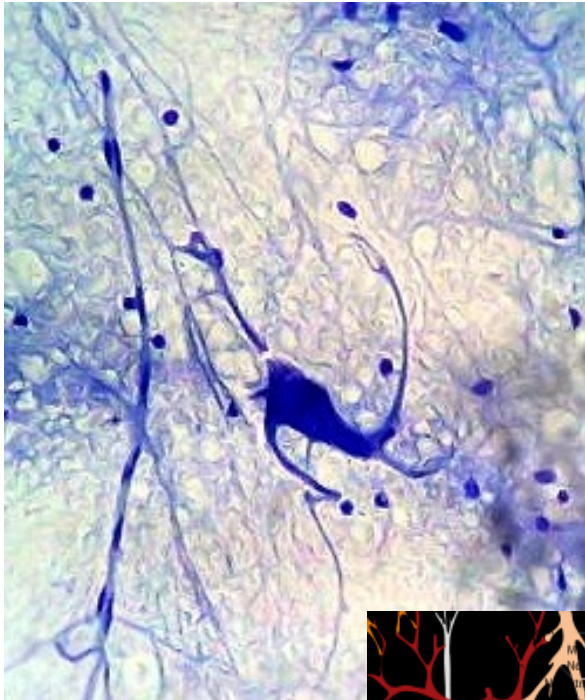


# Electric Circuit Theory

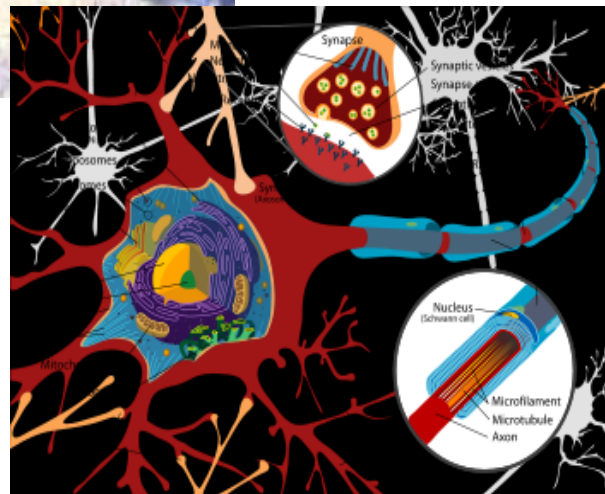
- The electrical quantities which describe the electrical behavior of a circuit are integral quantities (lumped parameters, macroscopic quantities that are voltages and currents). They depend on time but not on space. These quantities can be measured in physical circuits.
- The equations of the circuit models are ***algebraic equations*** or ***integrodifferential time dependent equations*** that in many cases can be reduced to algebraic equations.
- The quantities of the ***distributed parameter*** models are usually time and space dependent quantities (differential parameters, microscopic quantities that are electric fields and current densities). The equations of these models are partial differential equations in time and space.

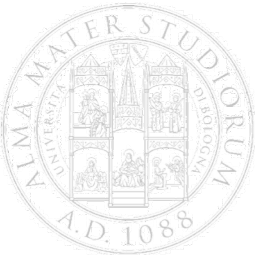
# Circuits

## in Nature and in Technological Applications



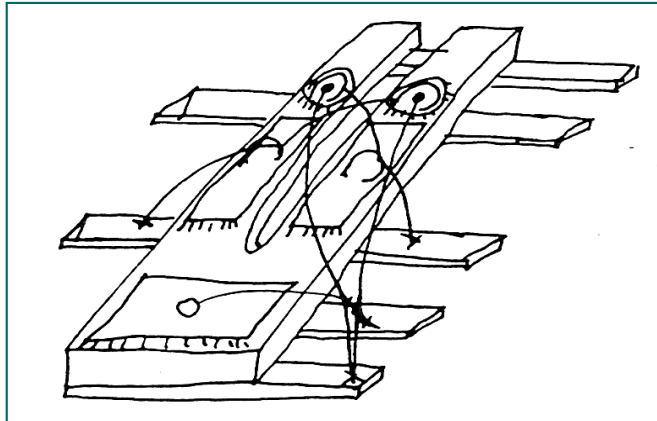
*Neurons  
and their  
connections*



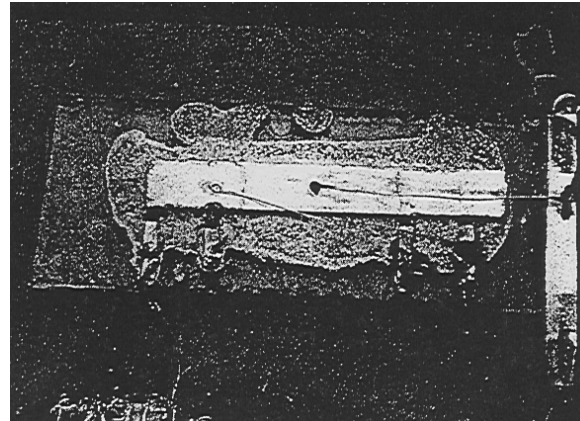


# Electric Circuit Theory

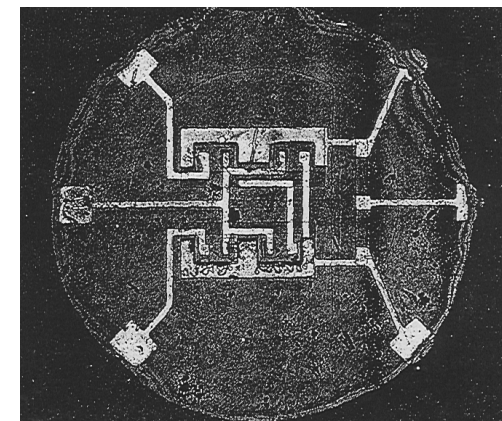
- ❑ In 1958 the first **chip** was realized by J. St. Clair Kilby (Fig. A, B – **chip** is an electronic device that contains several solid state circuit elements).
- ❑ In 1961 the first **monolithic chip** (Fig. C) was made . The realization of monolithic circuits with several circuit elements (**integrated circuits**) allowed to reduce the dimensions of the electric and the electronic devices followed by a rapid development of the technology in this field.



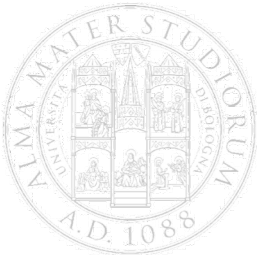
**A**



**B**

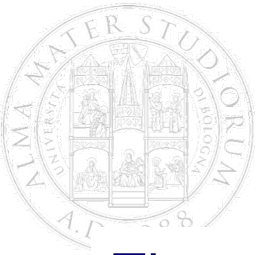


**C**



# Electric Circuit Theory

- The characteristics of the electromagnetic fields and the properties of the materials (electric conductive, semi-conductive and insulating materials) allowed to develop the very powerful technology of the electric circuits which are studied by means of the electric circuit theory.
- The electric circuits are used in many technological applications for the treatment of information signals and of power (electrical and electronic devices, computers, control devices, telecommunication systems, electric power systems).



# Electric Circuit Theory

The **circuit theory** aims to simulate and to predict the **electrical behavior** of the physical circuits for the analysis and the design of them (to enhance the performance, to decrease their cost, to analyze all working conditions, to study the fault conditions, the thermal effects, the endurance, etc.)

## Applications

- ✓ **dimensions:** integrated circuits, hi-fi circuits, computers, electronic devices, telecommunication systems, electrical power generation, transmission and utilization systems ( $10^{-3}$  -  $10^6$  m);
- ✓ **tension:**  $10^{-6}$  V (noise analysis devices) -  $10^6$  V (electrical power systems);
- ✓ **current:**  $10^{-15}$  A (1 fA: electrometers) -  $10^6$  A (power systems);
- ✓ **frequency:** 0 (direct current) -  $10^9$  Hz (1 GHz: microwave circuits, computers);
- ✓ **power:**  $10^{-14}$  W (radio signals from galaxies) -  $10^9$  W (electric power stations).





# Integral form of the EM Equations *to be considered for the Electric Circuit Theory*

➤ An electric circuit, made of interconnected circuit elements (resistors, inductors, capacitors, diodes, transistors operational amplifiers), operates through the EM phenomenology.

$$\oint \mathbf{H} \cdot d\mathbf{l} = i_t \quad \text{1st Maxwell's law} \quad \left( i_t = \iint_S \mathbf{J}_t \cdot \hat{\mathbf{n}} d\mathbf{S} \right)$$

$$\oint \mathbf{E} \cdot d\mathbf{l} = -\frac{d\Phi_B}{dt} \quad \text{2nd Maxwell's law} \quad \left( \Phi_B = \iint_S \mathbf{B} \cdot \hat{\mathbf{n}} d\mathbf{S} \right)$$

$$\oiint_S \mathbf{J} \cdot \hat{\mathbf{n}} d\mathbf{S} = -\frac{dq}{dt} \quad \text{charge conservation law}$$

$$\Rightarrow \oiint_S \mathbf{D} \cdot \hat{\mathbf{n}} d\mathbf{S} = q \quad \text{Gauss's law}$$

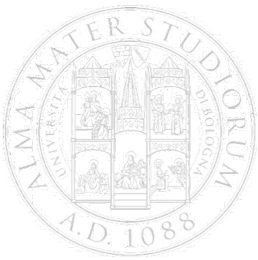
$$\Rightarrow \oiint_S \mathbf{J}_t \cdot \hat{\mathbf{n}} d\mathbf{S} = 0$$

$$\Rightarrow \oiint_S \mathbf{B} \cdot \hat{\mathbf{n}} d\mathbf{S} = 0$$

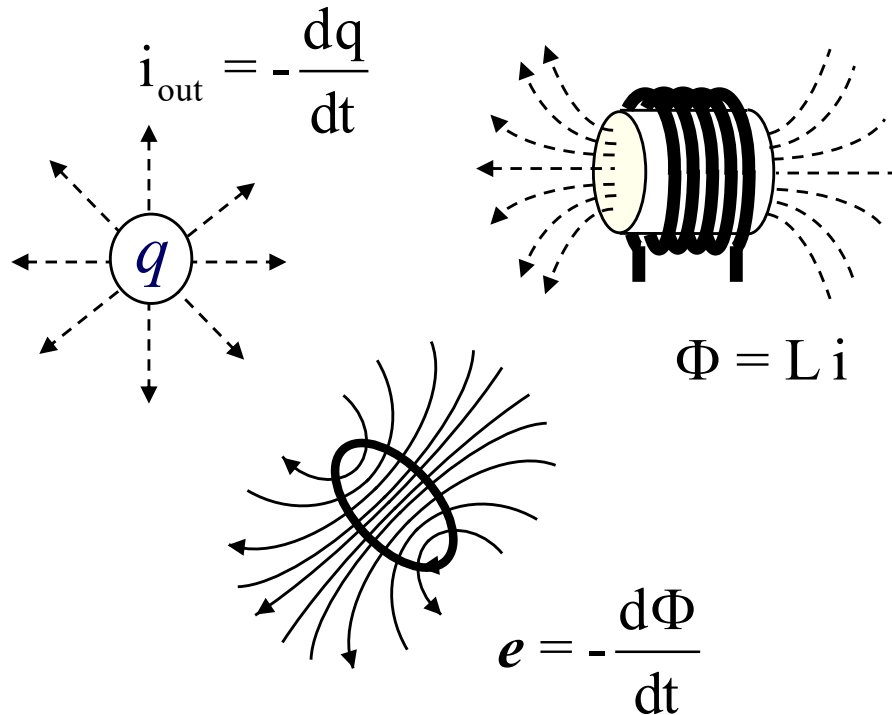
## Material laws

$$\begin{cases} \mathbf{H} = \mu \mathbf{B} \\ \mathbf{D} = \varepsilon \mathbf{E} \\ \mathbf{J} = \sigma \mathbf{E} \end{cases}$$





# Electric Circuit Theory



## Circuit element (circuit component):

it is a region of space inside which the electromagnetic phenomena are confined.

$$\mathbf{J} = \sigma \mathbf{E}$$

$$\rho = \frac{1}{\sigma}$$

Material	Resistivity $\rho$ , OHM-Meters	Resistivity Ohm-Meters	Regime
Sulfur	$2 \times 10^{15}$	$10^{16}$	Insulators
Si O <sub>2</sub>	$> 10^{14}$	$10^{14}$	
Al <sub>2</sub> O <sub>3</sub>	$10^{12}$	$10^{12}$	
Iodine	$1.3 \times 10^7$	$10^8$	Semiconductors
Cadmium	$1.8 \times 10^4$	$10^4$	
Germanium	0.46	1	
Tellurium	0.044	$10^{-2}$	Conductors (metals)
Mercury	$9.4 \times 10^{-7}$	$10^{-4}$	
Graphite	$6.5 \times 10^{-7}$	$10^{-6}$	
Iron	$10^{-7}$	$10^{-8}$	
Tungsten	$5.4 \times 10^{-8}$		
Aluminum	$2.73 \times 10^{-8}$		
Copper	$1.73 \times 10^{-8}$		
Silver	$1.63 \times 10^{-8}$		

**Interconnections:** they are conducting channels (usually cables) where the charges flows from a component to another..



# Electric Circuit Theory Assumptions

- The ***circuit model*** following the ***lumped component model approximation*** is based on the assumption of quasi-stationary approximation of the EM equations and on the properties of materials.

- Quasi-stationary approximation states that electric displacement **D** and magnetic flux density **B** do not vary in time simultaneously in the same place

$$\frac{\partial \mathbf{B}}{\partial t} \neq 0, \frac{\partial \mathbf{D}}{\partial t} = 0 \quad oppure \quad \frac{\partial \mathbf{B}}{\partial t} = 0, \frac{\partial \mathbf{D}}{\partial t} \neq 0$$

- Moreover due to the quasi-stationary assumption the propagation of the EM quantities is assumed to be instantaneous ( $\Delta t_{\text{propag}} = 0$ , the propagation velocity is assumed to be infinite).



# Electric Circuit Theory Assumptions

- A circuit is constituted by circuit elements connected by conductors. The circuit is immersed into an insulating material. The assumption of quasi-stationary EM, necessary to the lumped circuit model, may be done due to the materials constituting a circuit. Indeed here it is assumed that:

1. The time variation of the magnetic flux **outside circuit elements** is zero.

$$\frac{d\Phi}{dt} = 0$$

*B varies in time only inside elements  $\rightarrow \nabla \times \mathbf{E} = -\partial \mathbf{B} / \partial t$ :  
outside the elements  $\mathbf{E}$  is **conservative**, inside the  
elements  $\mathbf{E}$  is **not conservative**.*

2. The time variation of the charge **inside conductors**, where the conductivity is assumed to be infinite, is zero.

$$\frac{dq}{dt} = 0$$

*Inside conductors q does not varies in time as  $\sigma = \infty \rightarrow$  from  
Gauss law  $\partial \mathbf{D} / \partial t = \partial \rho_C / \partial t = 0 \rightarrow \mathbf{J} = \mathbf{J}_t$  and  $\mathbf{J}$  is **solenoidal**.*

- The EM phenomena are considered to be confined inside the circuit elements.



# Electric Circuit Theory Assumptions

As  $dq/dt = 0$  inside conductors and that they are immersed into insulating material allows to consider the conductors as flux tubes of  $\mathbf{J}$ . Moreover inside connectors the displacement current density  $\mathbf{J}_D = \partial \mathbf{D} / \partial t = 0$  and that  $\mathbf{J}_t = \mathbf{J} + \mathbf{J}_D = \mathbf{J}$ . Hence inside a conductor  $\mathbf{J}$  is **solenoidal** ( $\nabla \cdot \mathbf{J}_t = \nabla \cdot \mathbf{J} = 0$ ). Hence it follows:

- Due to the solenoidality of  $\mathbf{J}$  ***the current is constant in any cross section of a connector.***
- Moreover, due to the solenoidality of  $\mathbf{J}$  for a closed surface  $S$  which passes completely outside the circuit elements, it is:

$$\oiint_S \mathbf{J} \cdot \mathbf{n} \, dS = 0$$

The total flux of  $\mathbf{J}$  through  $S$  is given by the currents  $i_1, i_2, \dots, i_n$  flowing outside of the surface. Thus it is:

$$i_1 + i_2 + \dots + i_n = 0$$



---

***This is the Kickoff Current Law (KCL)***



# Electric Circuit Theory Assumptions

As  $d\Phi/dt = 0$  outside the circuit elements, outside the circuit elements the electric field is conservative and the tension in a closed line not intersecting circuit elements is zero:

$$\oint \mathbf{E} \cdot d\mathbf{l} = - d\Phi/dt = 0$$

Hence it follows that:

- The electrical tension (the voltage) between two points connected by a line in conductors connecting circuit elements running outside of them, is given by the electric potential difference of the potential in the two points:

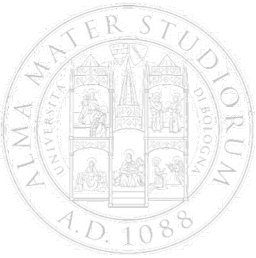
$$\int_1^2 \mathbf{E} \cdot d\mathbf{l} = v_1 - v_2 = v_{12}$$

- For a closed line connecting the points in conductors, running outside the elements, it is :

$$v_{12} + v_{23} + \dots + v_{n,1} = 0$$



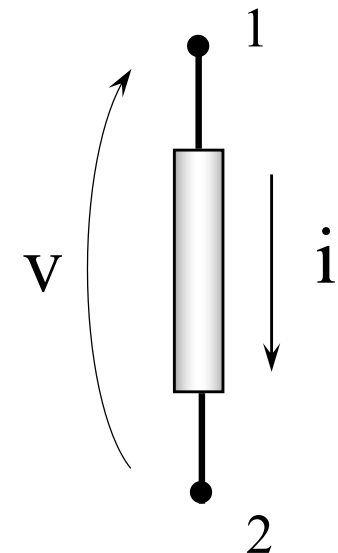
*This is the Kickoff Tension Law (KTL)*



# Circuit Element

## The Two Terminal Circuit Element

- The ***two terminal circuit element*** (also said ***dipole***) is a circuit element consisting of a closed surface  $S$  from which two terminals come out. All EM phenomena are active inside  $S$ . Here  $\mathbf{E}$  can be non-conservative and it can vary ( $\mathbf{B}$  or  $\mathbf{D}$  varies in time). Outside  $S$   $\mathbf{D}$  and  $\mathbf{B}$  are constant in time and all EM phenomena are silent. Hence the  $\mathbf{E}$  field is conservative.  $S$  has a shielding effect. In a circuit the circuit elements are connected to each other through conductors connecting their terminals.
- The status of the element is described by current and tension:  $i$ ,  $v$ .
- As a consequence of the charge conservation law the current flowing through an element is the current entering from a terminal and going out from the other one.
- Outside  $S$ ,  $\mathbf{E}$  is conservative. Therefore outside  $S$  a potential function  $v$  exists and is defined by  $v_1$  and  $v_2$ , which are the values of  $v$  at terminal 1 and 2.  $v_{12} = v_1 - v_2$  is the potential difference (or the voltage) between them.



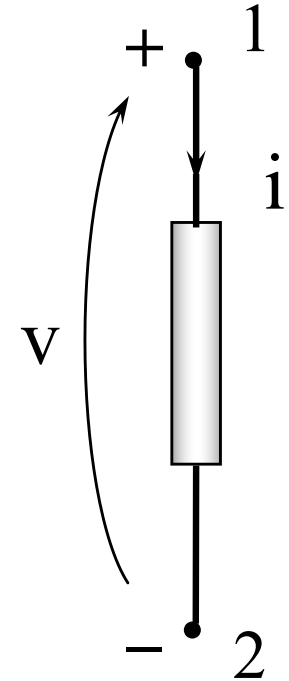




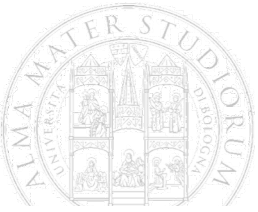
# Characteristics of the Two Terminal Element

## Circuit Element

- The two terminal element (dipole) is connected through its two terminals to the other circuit elements of a circuit. The connections are the **nodes** of the circuit. The circuit elements are the **branches** of the circuit.
- $v_1 > v_2 \rightarrow v = v_1 - v_2 > 0$  is the **branch tension** (or **branch voltage**).
- $i$  is the **branch current**.  $i$  is assumed to be positive when it enters into the terminal at higher potential (terminal 1 in this case) and goes out from terminal at lower potential.
- The relation between  $v$  and  $i$ , given by the **element equation**  $v = f(i)$ , is the circuit element characteristic.
- The work made by the field  $\mathbf{E}$  on the charges which flows through a branch cross section per time unit is the **two terminal element electrical power**:



$$p(t) = \lim_{\Delta t \rightarrow 0} \left( \int_1^2 \Delta q \mathbf{E} \cdot d\mathbf{l} / \Delta t \right) = \frac{dq}{dt} \int_1^2 \mathbf{E} \cdot d\mathbf{l} = i(t) v(t)$$



# n-Terminal Circuit Element

Electric circuit element having  $n$  terminals with  $n$  greater than two is said ***n-terminal circuit element***. A reference terminal is defined (M.0).

- As a consequence of the charge conservation:

$$i_0 = i_1 + i_2 + \dots + i_{n-1}$$

- As **E** is conservative outside of the  $n$ -terminal the following definitions are made:

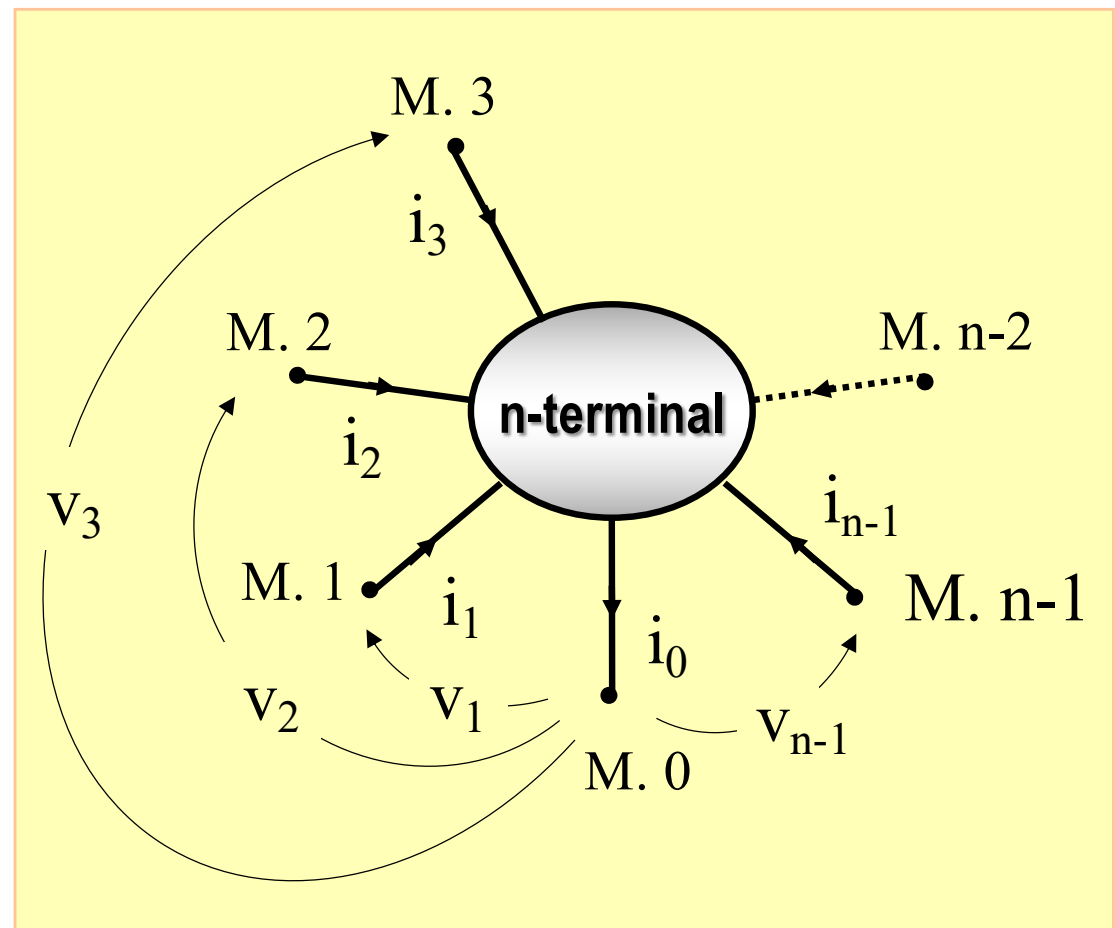
$$V_1 = V_{M.1} - V_{M.0}$$

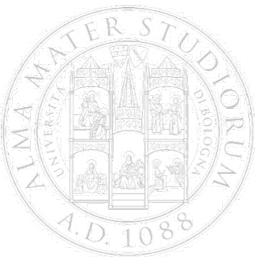
$$V_2 = V_{M.2} - V_{M.0}$$

$$V_3 = V_{M.3} - V_{M.0}$$

.....

$$V_{n-1} = V_{M.n-1} - V_{M.0}$$

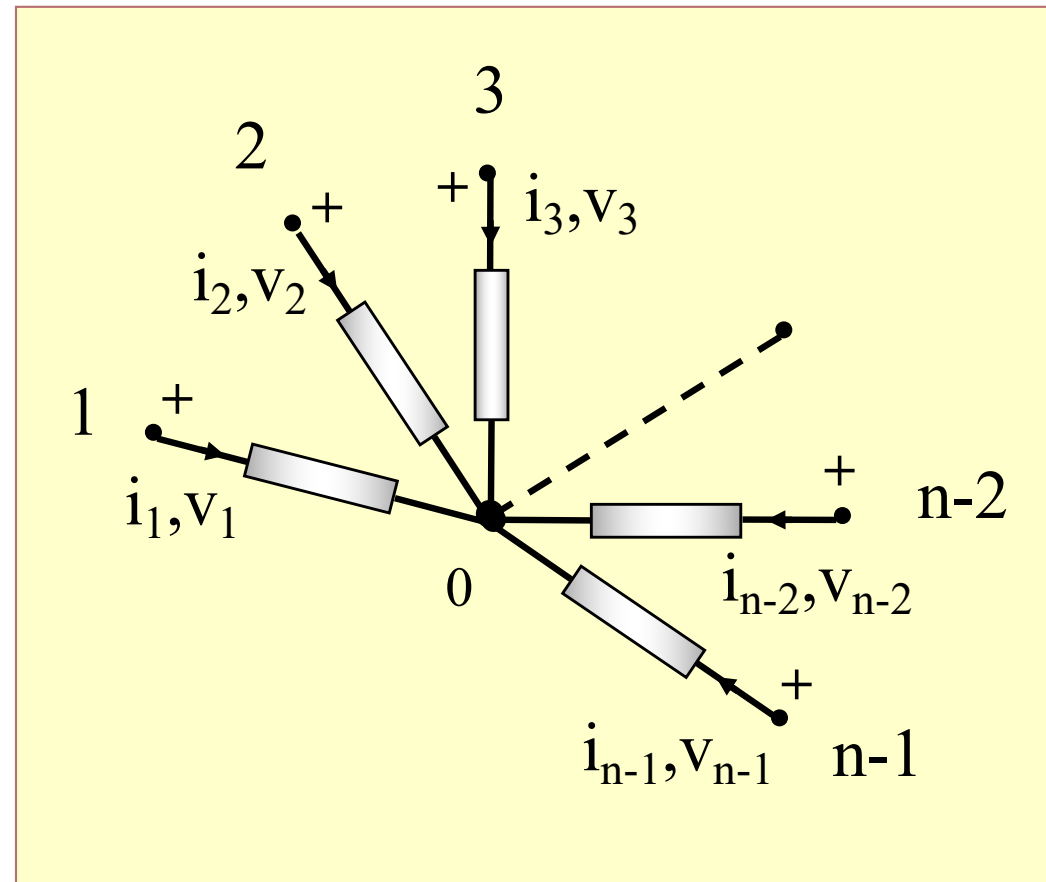




# n-Terminal Circuit Element

A n-terminal is described by  $n-1$  pairs of values,  $n-1$  currents and  $n-1$  voltages. Hence it is equivalent to  $n-1$  two terminal elements with a common terminal.

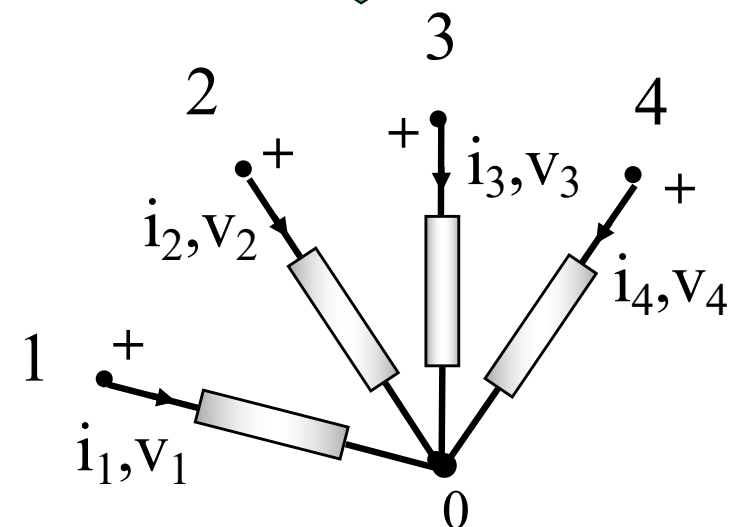
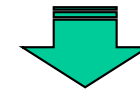
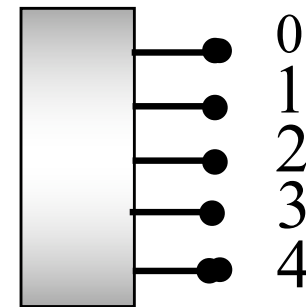
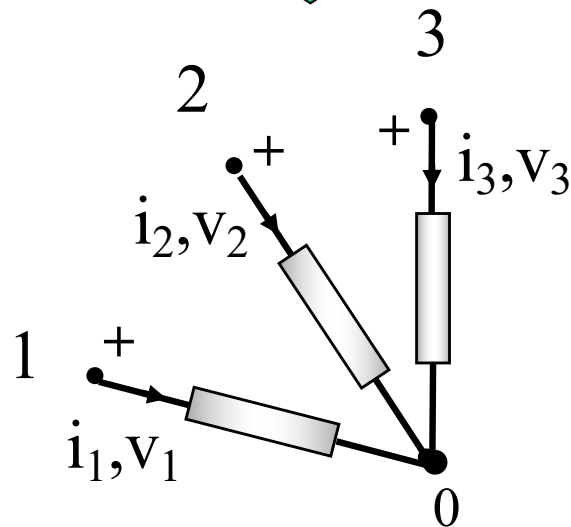
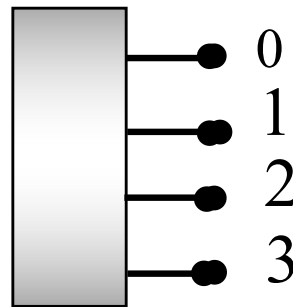
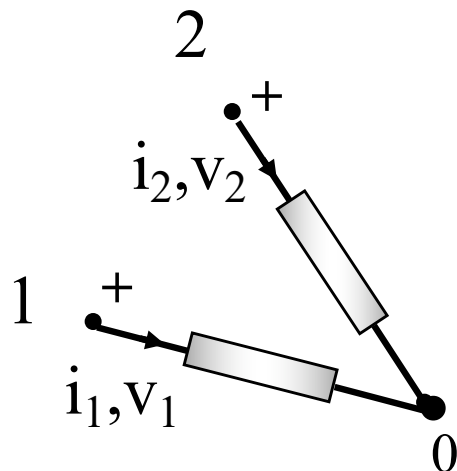
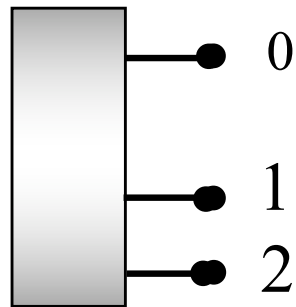
$$i_1, i_2, i_3, \dots, i_{n-1}$$
$$V_1, V_2, V_3, \dots, V_{n-1}$$

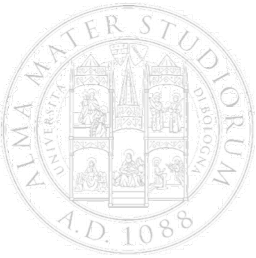




# n-Terminal Circuit Element

A three terminal element corresponds to two dipoles with two common terminals. A four terminal element corresponds to three dipoles with three common terminals etc.

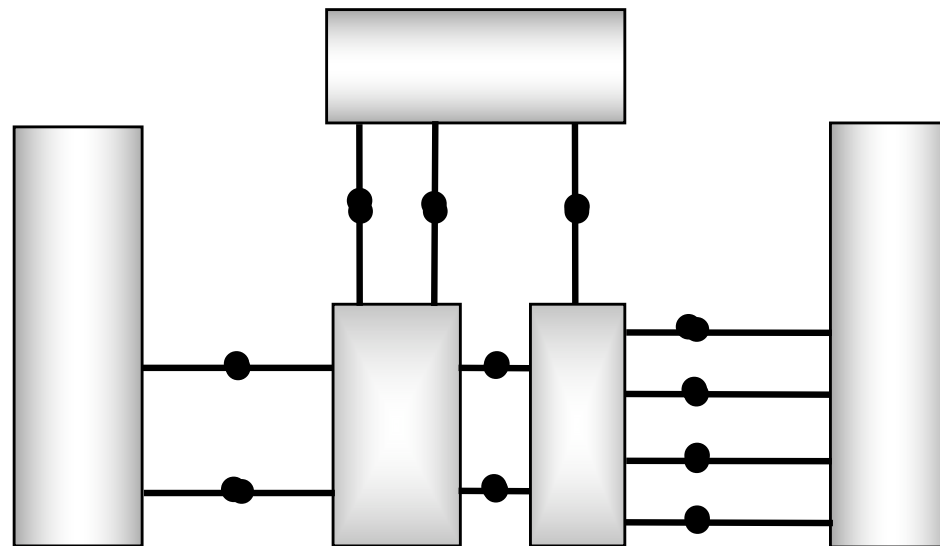




# Electric Circuit

In a circuit the interconnections among the circuit elements are realized through conductive cables (assumed as ideal conductors,  $\sigma = \infty$ ). In the circuit theory the elements are the **branches** of the circuit. The interconnections are the **nodes** of the circuit. A circuit is characterized by ***n*** nodes and ***r*** branches.

***Circuit with 11 nodes ( $n = 11$ ) and 17 branches ( $r = 17$ )***





# Electric Circuit

- ❖ A circuit is made of circuit elements connected through conductive cables (a conductor). Dipoles (two terminal circuit elements) are the **branches** of the circuit. The interconnections are the **nodes**.
- ❖ The EM phenomena are confined inside the circuit elements where **B** or **D** can vary in time. Outside of the circuit elements **D** and **B** are constant in time:  
*Outside c.elements:*  $\partial \mathbf{D} / \partial t = 0 \rightarrow dq/dt = 0 \rightarrow \nabla \cdot \mathbf{J} = 0 \rightarrow \text{KCL}$   
*Outside c.elements:*  $\partial \mathbf{B} / \partial t = 0 \rightarrow d\Phi/dt = 0 \rightarrow \nabla \times \mathbf{E} = 0 \rightarrow \text{KTL}$   
*In any cross section of a conductor* the current is constant.
- ❖ A dipole is described by branch current  $i$  and branch tension  $v$ . The branch current  $i$  is assumed to be positive if it enters into the dipole from the positive terminal (terminal of the dipole at higher electrical potential). The relation between  $v$  and  $i$ , given by  $v = f(i)$ , is the characteristic of the circuit element.
- ❖ The conductors connecting circuit element are assumed to be ideal ( $\sigma = \infty$ ), The propagation time between the connected elements is zero. As a consequence the change of any electrical quantity in an element is immediately transferred to the other elements connected to it.





# Kirchhoff's Laws

## Kirchhoff's Current Law (KCL)

A closed surface  $S_C$ , which passes through some nodes of the circuit but not through circuit elements, is considered. No charge variation is assumed inside conductors and  $\mathbf{J}$  here is solenoidal. Hence for the charge conservation law the total currents entering  $S_C$  is equal to zero:

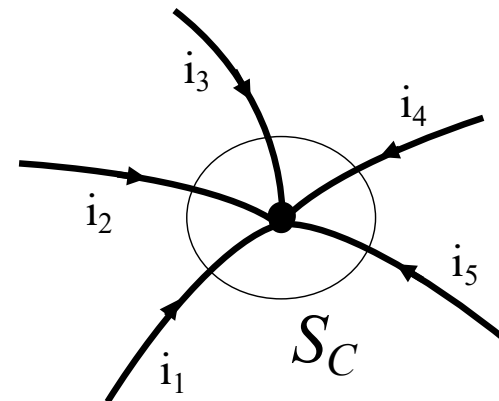
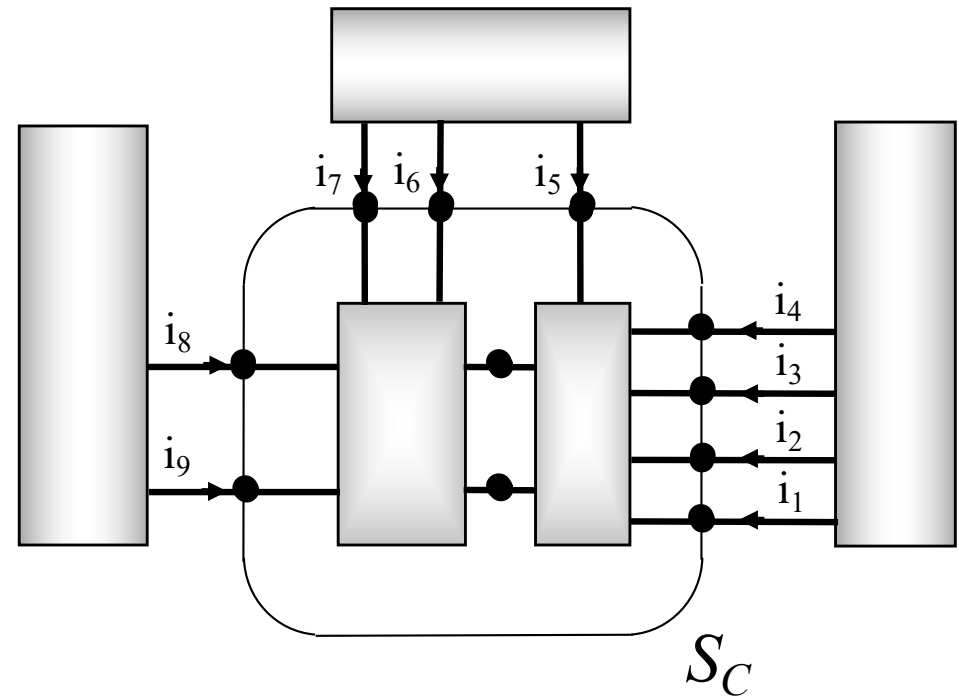
$$i_1 + i_2 + i_3 + \dots + i_9 = 0$$

When  $S_C$  contains only one node, the following corollary is obtained:

$$\sum_{k=1}^n i_k = 0$$

Node equation

*The algebraic sum of the currents entering a node is equal to zero.*





# Kirchhoff's Laws - Node Equations

For the nodes A, B, and C from the KCL it is:

$$i_1 - i_4 - i_5 = 0 \quad (A)$$

$$i_1 + i_2 = 0 \quad (B)$$

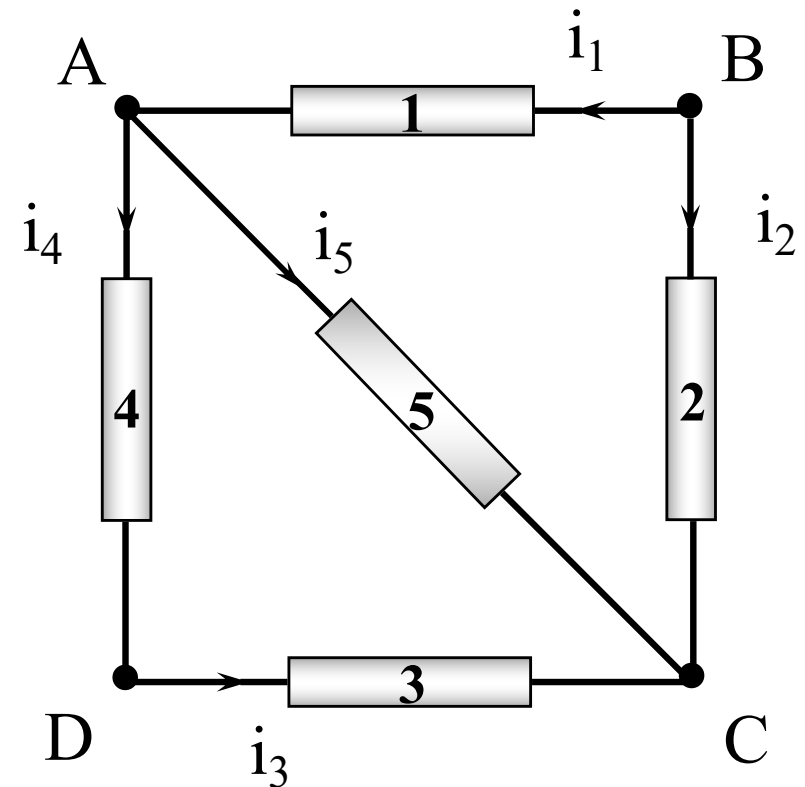
$$i_2 + i_3 + i_5 = 0 \quad (C)$$

Each node equation takes into account at least a new current which does not appear in the other equations. In the equation derived from the KCL for node D only currents appear which are present in the equations of the other three nodes. This equation is a linear combination of the others. For node D it is:

$$i_3 - i_4 = 0 \quad (\text{this is eq. C} + \text{eq. A} - \text{eq. B})$$

This is stated as follows:

***In a circuit  $n-1$  node equations are linearly independent.***





# Kirchhoff's Laws

## Kirchhoff's Tension Law (KTL)

The field  $\mathbf{E}$  is conservative in the region outside the circuit elements. Therefore the sum of the potential differences on a closed path  $l_C$ , which connects nodes and does not intersect circuit elements, is equal to zero.

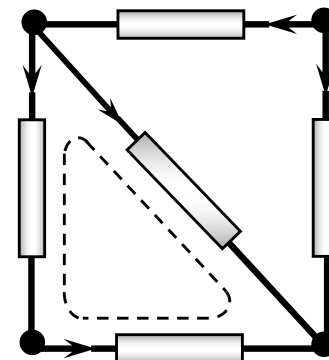
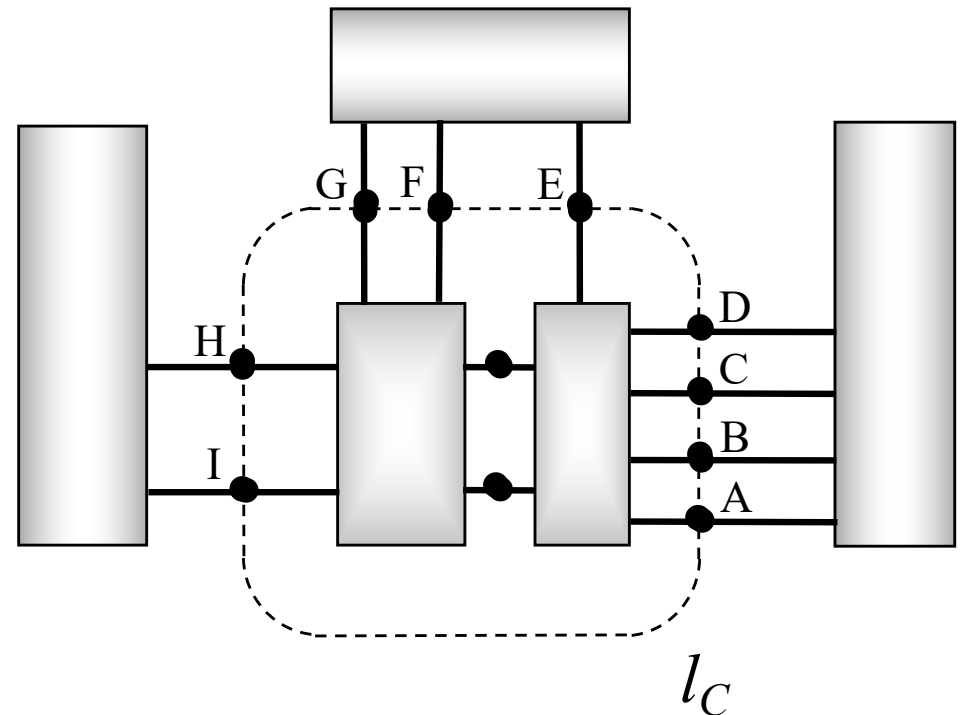
$$V_{AB} + V_{BC} + \dots + V_{HI} + V_{IA} = 0$$

When  $l_C$  connects the nodes of a circuit loop, the following corollary is obtained. :

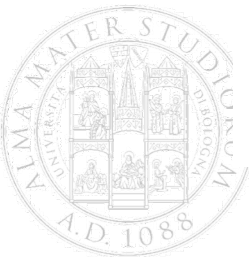
*The algebraic sum of the voltages of the branches of a loop is equal to zero.*

$$\sum_{k=1}^m v_k = 0$$

↖ *Loop equation*



*In an electric circuit a loop is defined as a closed path passing only once through every node in the path.*



# Kirchhoff's Laws - Loop Equations

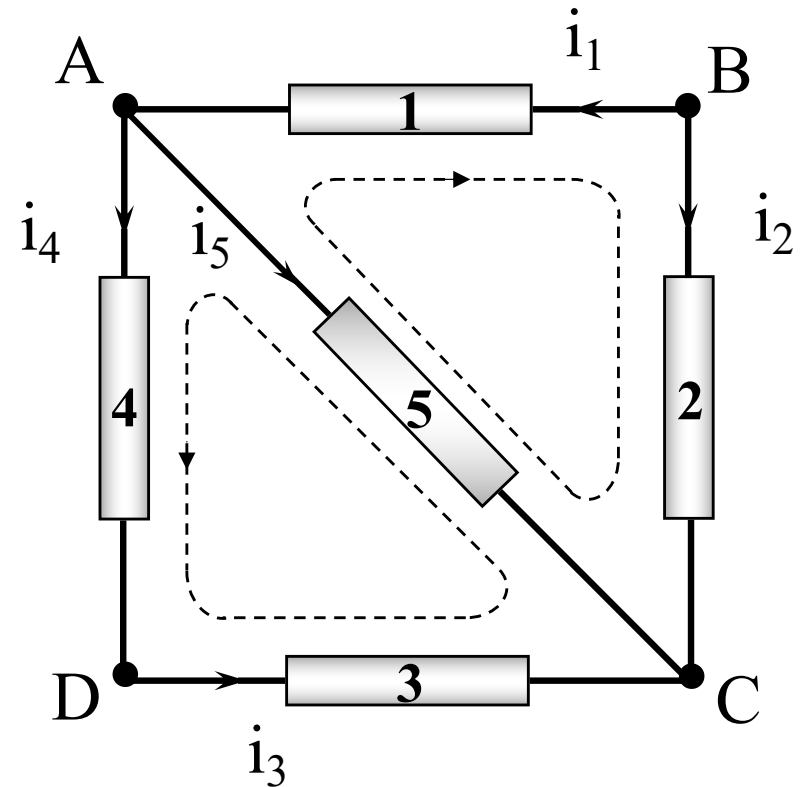
The loops ADCA and ABCA are considered. From the KTL it follows

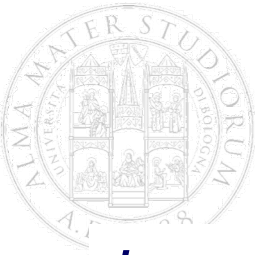
$$-v_4 - v_3 + v_5 = 0 \quad (a)$$

$$v_1 - v_2 + v_5 = 0 \quad (b)$$

Each equation takes into account at least a new voltage which does not appear in the other equations. In the equation derived from the KTL for loop ABCDA only voltages appear, which are present in the above equations. This equation is a linear combination of the two above mentioned equations. This is stated as follows:

***In a circuit  $r-n+1$  loop equations are linearly independent.***





# Node and Loop Equations

*In a electric circuit of  $r$  dipoles, hence of  $r$  branches and  $n$  nodes there are  $r-n+1$  independent loop eq.s and  $n-1$  node eq.s.*

In the circuit of the figure for each node and each loop it is:

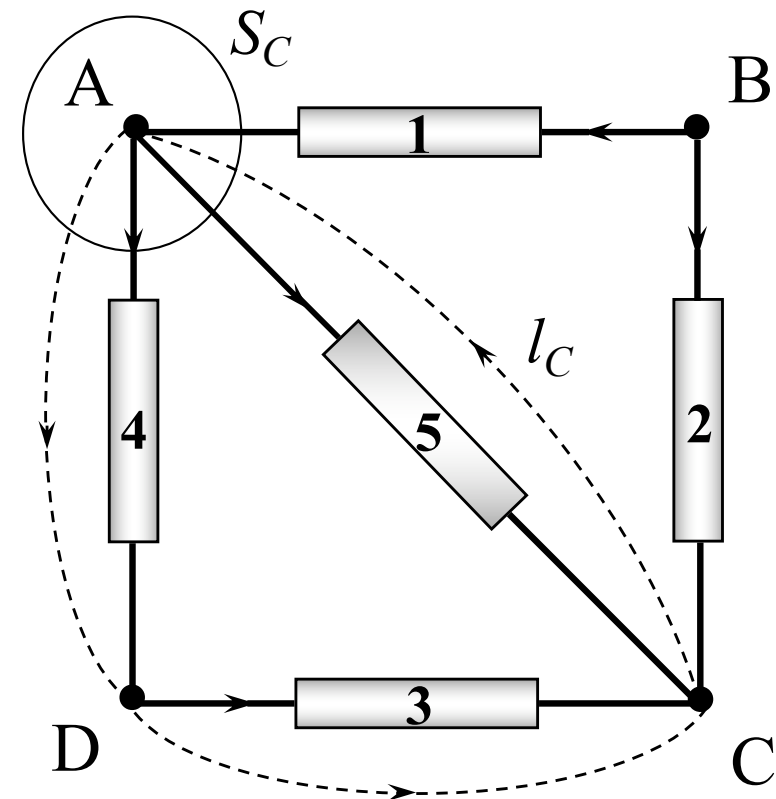
$$\begin{array}{ll} \text{KCL for each node:} & \sum_n i_n = 0 \\ \text{KTL for each loop:} & \sum_m v_m = 0 \end{array}$$

***In the figure  $r = 5$  and  $n = 4$ :***

- $n - 1 = 3$  linearly independent node equations,
- $r - n + 1 = 2$  linearly independent loop equations

The equations are of the following type:

- for loop ADCA:  $v_5 - v_3 - v_4 = 0$
- for node A:  $i_4 + i_5 - i_1 = 0$





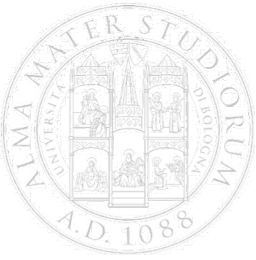
# Topology Equations

The status of a circuit of  $r$  branches and  $n$  nodes is described by  $r$  branch currents and  $r$  branch voltages, which are  $2r$  quantities. Therefore  $2r$  ( $r$  branch currents and  $r$  branch voltages) are the unknown quantities of the analysis problem.

From the topology of the circuit, given by the number of branches, the number of nodes, and their connections,  $r$  linearly independent topology equations are obtained.  $n-1$  are the node equations derived from the KCL.  $r-n+1$  are the loop equations derived from the KTL.

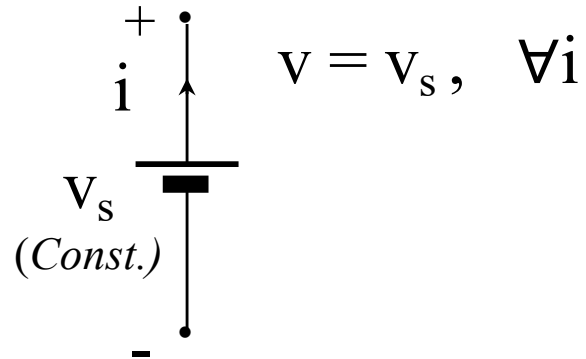
*In order to have an unique solution of the analysis problem, other  $r$  equations are necessary. They are given by the element equations which state the relation between current and voltage for each branch.*



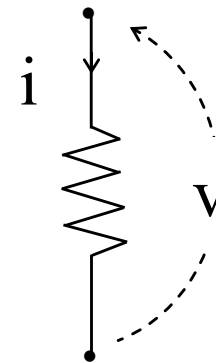


# Element Equations

## Independent Voltage Source



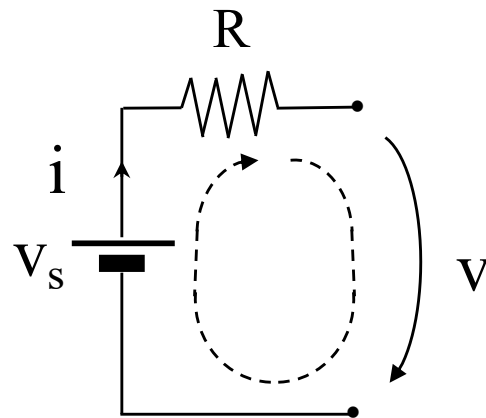
## Ideal Resistor



$$v(t) = R i(t)$$

[Ohm's law]

$R$  [ $\Omega$  (Ohm)]  
*electric resistance*



$$v + v_s - R i = 0$$

$$\Rightarrow v = R i - v_s$$



# Circuit analysis: an example

## The problem of analysis

The topology of the circuit and the independent voltage sources ( $v_{s1}$ ,  $v_{s2}$ ) and the resistances ( $R_1$ ,  $R_2$ ,  $R_3$ ) are the problem input. The branch voltages and the branch currents ( $v_1$ ,  $v_2$ ,  $v_3$ ,  $i_1$ ,  $i_2$ ,  $i_3$ ) are the problem output.

1. Direct the branches (define the positive direction of the currents  $i_1$ ,  $i_2$ ,  $i_3$ ).
2. Define the nodes and the loops for the independent topology equations (in the circuit of the figure:  $n-1=1$ ;  $r-n+1=2$ ).
3. Define the direction of each loop.
4. Write the topology equations (KCL and KTL) and the element equations:

$$i_1 - i_2 - i_3 = 0$$

$$-v_1 - v_2 = 0$$

$$v_2 - v_3 = 0$$

$$v_1 = R_1 i_1 - v_{s1}$$

$$v_2 = R_2 i_2 + v_{s2}$$

$$v_3 = R_3 i_3$$

( these are 6 eq.s in 6 unknown  
with an unique solution)



By substituting the element  
eq.s into the topology eq.s:

$$i_1 - i_2 - i_3 = 0$$

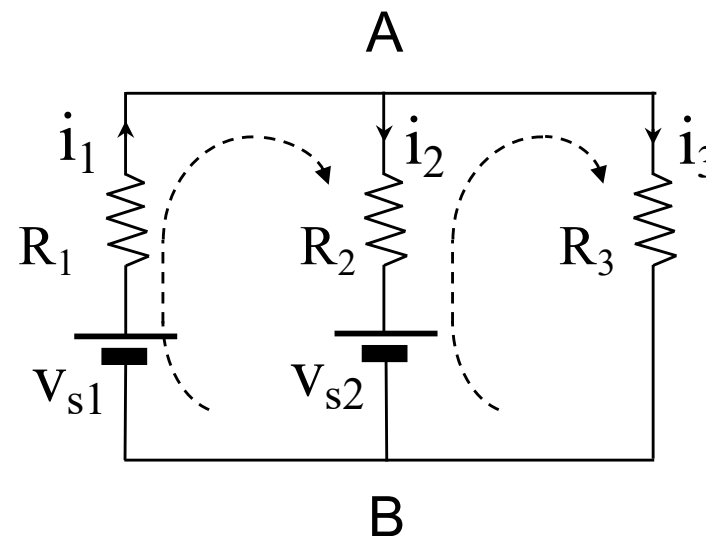
$$-R_1 i_1 - R_2 i_2 = -v_{s1} + v_{s2}$$

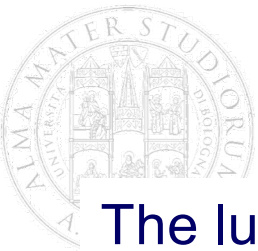
$$R_2 i_2 - R_3 i_3 = -v_{s2}$$



From which the branch  
currents are obtained  
( $i_1$ ,  $i_2$ ,  $i_3$ ).

By substituting the  
currents  $i_1$ ,  $i_2$ ,  $i_3$  into the  
element eq.s the  
branch voltages  $v_1$ ,  $v_2$ ,  
 $v_3$  are derived.





# Lumped Circuit Approximations

The lumped electric quantities in a circuit can have rapid or slow time variations in comparison with the **propagation times** within the circuit.

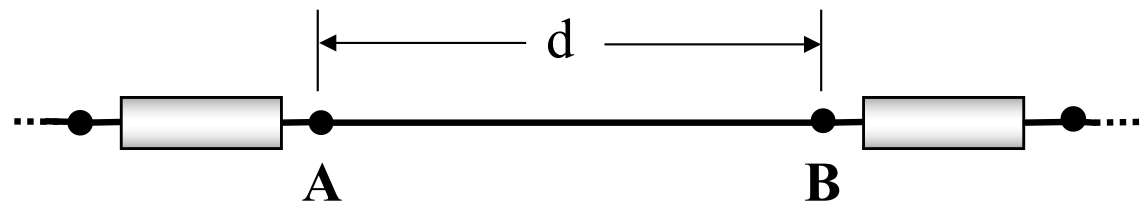
➤ The assumption of **lumped circuit** is that the propagation times within the circuit are much smaller than the time variation of the electric quantities. When treating waves (electric quantities expressed by sinusoidal functions in time), the lumped circuit assumption considers them propagating instantaneously within the circuit.

➤ The transit time for the propagation of a signal

from A to B is  $t_{AB} = d/v =$

$= d/c$  ( $v = c = 2,998 \times 10^8$  m/s is

the speed of light). For a sinusoidal signal with a frequency  $f$  and period  $T$  with  $f = 1/T$ , and wave length  $\lambda = c/f$ , it is  $T = \lambda/c$  and the time interval of an EM signal to go from A to B is  $t_{AB} = d/c$ . The assumption of lumped circuits to be verified needs that:



$$t_{AB} \ll T \quad \longleftrightarrow \quad d \ll \lambda$$



# Lumped Circuit Approximations

- ✓ Electrical power:  $f = 50/60 \text{ Hz} \rightarrow \lambda = 6000/5000 \text{ km}$
- ✓ Microwaves:  $f = 100 \text{ MHz} \rightarrow \lambda = 3 \text{ m}$
- ✓ Computer clock:  $f = 3 \text{ GHz} \rightarrow \lambda = 10 \text{ cm}$

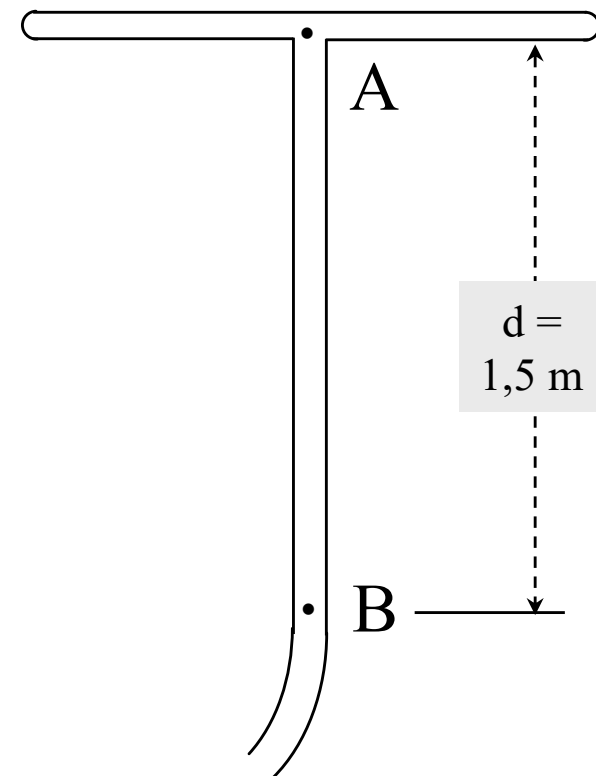
The antenna of the figure receives a signal of a frequency of 100 MHz corresponding an angular frequency  $\omega = 2\pi f = 2\pi \times 10^8$ .

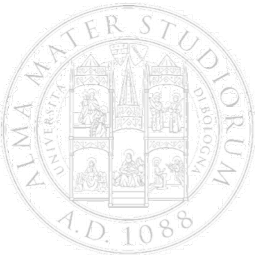
In A at the time  $t$  there is a tension of:

$$v_A(t) = V_0 \sin \omega t = V_0 \sin(2\pi \times 10^8 t)$$

In B the signal arrives after a time  $\Delta t = d/c = 1,5/3 \times 10^8 = 0,5 \times 10^{-8} \text{ s}$ . Therefore in B at the time  $t$  there is the signal that left A at the time  $t - \Delta t$ :

$$\begin{aligned} v_B(t) &= v_A(t - \Delta t) = \\ &= V_0 \sin[2\pi \times 10^8 (t - \Delta t)] = \\ &= V_0 \sin[2\pi \times 10^8 (t - 0,5 \times 10^{-8})] = \\ &= V_0 \sin(2\pi \times 10^8 t - \pi) = \\ &= -V_0 \sin(2\pi \times 10^8 t) = -v_A(t) \end{aligned}$$



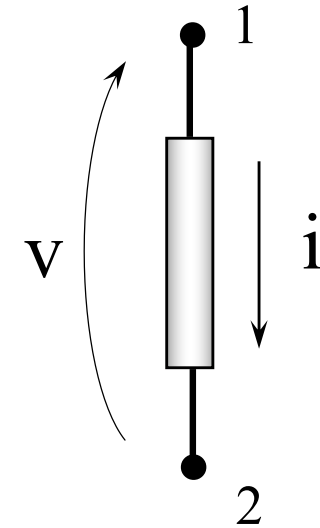


# Circuit Elements

## Two Terminal Element - Dipole

### ■ Element Equation

The relation between the branch current  $i$  flowing through the element and the branch voltage  $v$ , which is the potential difference between the terminals of the circuit element, defines the behavior of that element within the circuit. This relation is the **element equation** (said also the ***i-v characteristic***).



### Current controlled element

$$v = f(i)$$

the current is the independent variable.

### Voltage controlled element

$$i = g(v)$$

the voltage is the independent variable.

# Circuit Elements

## Two Terminal Elements

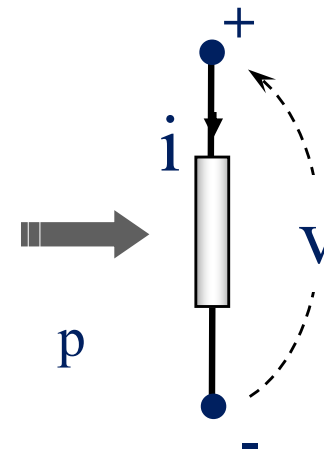
### ■ Element Equation

The ***element equation***, which defines the relation between the branch current  $i$  flowing through the element and the branch  $v$  between the terminals of the circuit element, is determined by the physical phenomena caused by of the element.

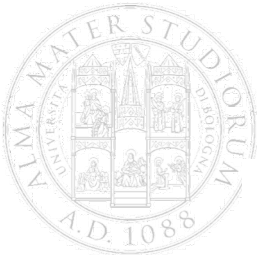
### ■ Passive Two Terminal Elements

In the ***passive element convention*** the current enters into the element from the positive terminal. In the element, as in resistors, the charge is displaced from the higher potential to the lower potential due to the positive potential difference. Therefore the energy results to be dissipated. *In passive elements the energy is always positive or equal to zero.*

*Passive dipole convention*



$$w(t) = \int_{-\infty}^t v(t') i(t') dt' \geq 0$$



# Circuit Elements

## Two Terminal Elements

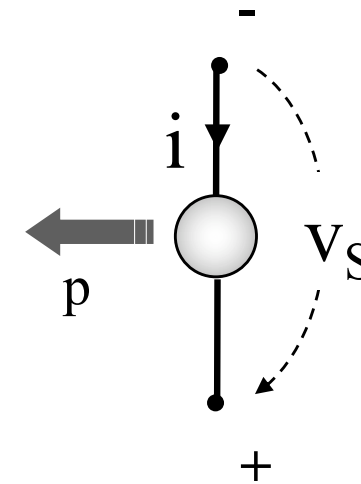
### ■ Active Two Terminal Elements

In the **active element** the current enters into the element from the negative terminal. The current flows from the negative to the positive terminal. The circuit element is doing work in moving charge from a lower potential to a higher potential.

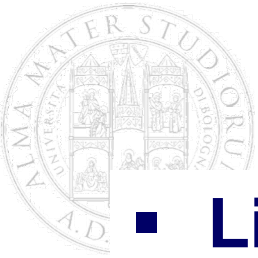
*Electric power sources (**tension sources** and **current sources**) are active elements.*

*(As stated by the passive element convention the branch voltage  $v = -V_S$ )*

*Active dipole*







# Element Equations

- **Linear and non-linear two terminal elements**

- ❑ **Linear element:** the element equation consists of linear operators.

example: 
$$v(t) = a + b i(t) + c \frac{di}{dt} + d \int_{t_0}^t i(t) dt \quad (1)$$

- ❑ **non-linear element:** the element equation is non-linear

example: 
$$v(t) = a' + b' i^2(t) \quad (2)$$

- **Time-independent and time-dependent elements:**

- ❑ **time-independent elements:** the element equations do not depend on time (in eq.s 1 and 2  $a, b, c, d, a'$  and  $b'$  are constant).
- ❑ **time-dependent elements:** the element equations are time-dependent (in eq.s 1 and 2  $a, b, c, d, a'$  and  $b'$  depend on time).



# Element Equations

## ■ Dissipative and Storage Elements

- **Elements without memory – dissipative elements:** the element equation expresses the relation between  $i$  and  $v$  at the same time  $t$  (In this case the passive elements are dissipative).

$$v(t) = a + b i^2(t) + c \sin [i(t)] \quad (\text{non-linear without memory})$$

- **Elements with memory – storage elements:** the element equation expresses the relation between  $i$  and  $v$  at different times.

$$v(t) = a + b i(t) + c \frac{di}{dt} \quad (\text{linear dipole with memory})$$

$$v(t) = \int_{-\infty}^t a i(t') dt' \quad (\text{linear dipole with memory})$$

The elements with memory store energy, which can be retrieved at a later time. These elements are also called **storage elements**.

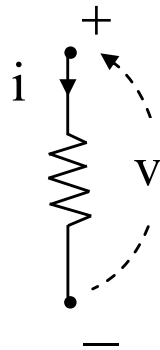


# Ideal Elementary Elements

- Resistors, capacitors, inductors, tension sources, and current sources.

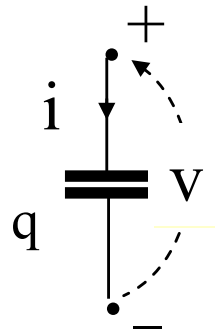
*The **Ideal Elementary Passive Elements** are the ideal resistor, the ideal capacitor and the ideal inductor described by the linear expressions given below. Each of these ideal elementary two terminal elements represents a single elementary EM process. In a real element a single elementary process is never present.*

**Ideal  
resistor**



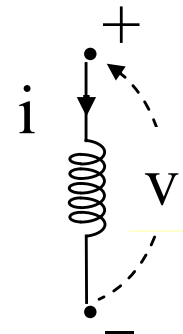
$$v(t) = R i(t)$$

**Ideal  
Capacitor**



$$v(t) = \frac{1}{C} \int_{-\infty}^t i(t') dt'$$

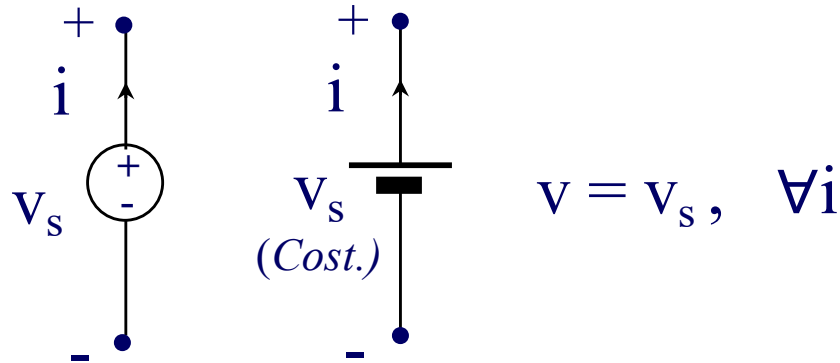
**Ideal  
inductor**



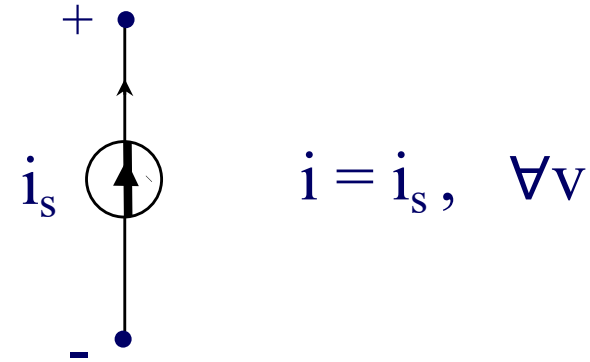
$$v(t) = L \frac{di}{dt}$$



## Ideal Tension Source (Independent Tension Source)



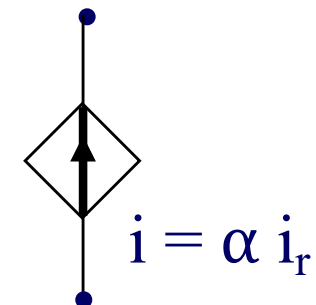
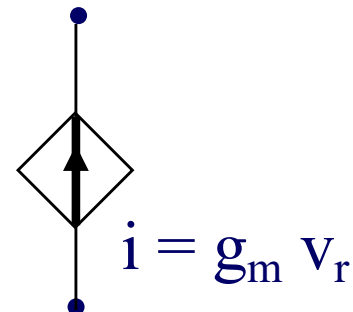
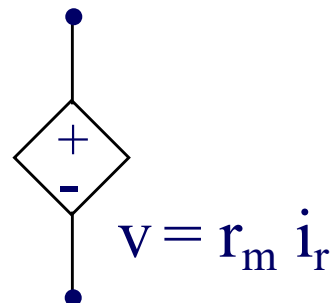
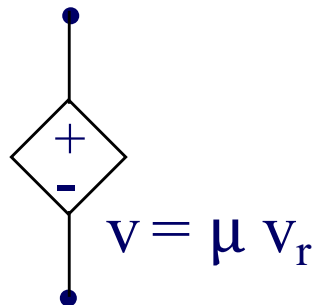
## Ideal Current Source (Independent Current Source)



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## Controlled Sources (Dependent Sources)

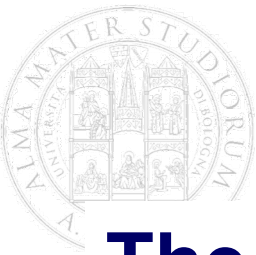


*Tension controlled  
tension source*

*Current controlled  
tension source*

*Tension controlled  
current source*

*Current controlled  
current source*

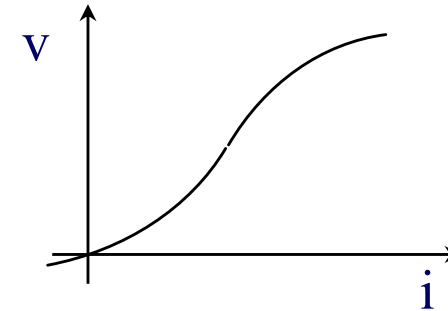
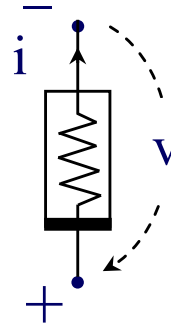


# Elementary Ideal Dipoles

## The Resistor

The resistor is a passive element which dissipates energy.

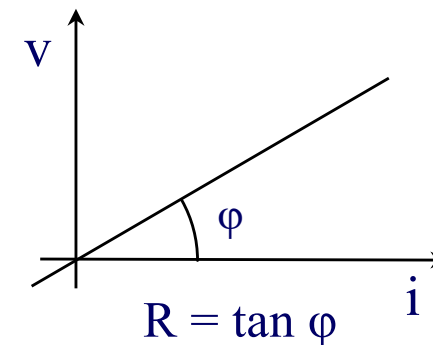
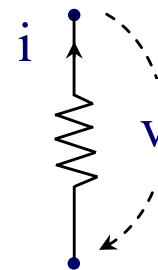
$$v = f(i)$$



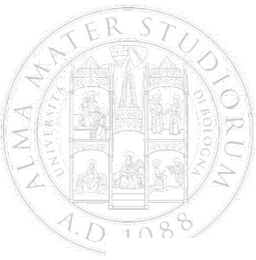
□ **Linear time independent resistor** (dissipative passive element):

$$v(t) = R i(t) \quad [\text{Ohm's law}]$$

$$p(t) = v(t) i(t) = R i(t)^2$$



- $R$  **resistance** (SI unit *ohm* [ $\Omega$ ]),
- $G = 1/R$  **conductance** (SI unit *siemens* [S]),



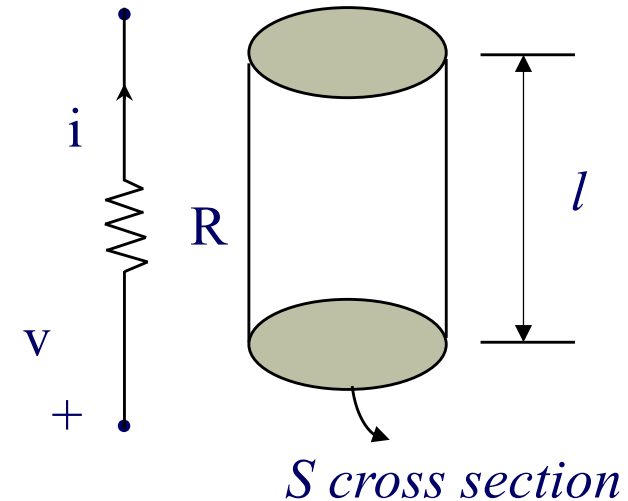
# The Linear Resistor

The **electrical resistance** of a circuit element is the parameter that quantifies its property to oppose the current:

$$v = R i \quad \Longleftrightarrow \quad R = v/i$$

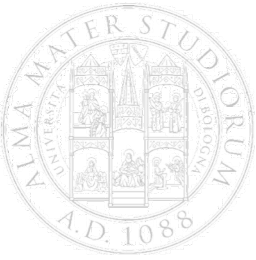
The **resistivity**  $\rho$  [ $\Omega\text{m}$ ] of a material quantifies its property to oppose the flow of electrical charges:

$$\rho = \frac{1}{\sigma} \quad \text{where } \sigma \text{ [S/m] is the } \mathbf{electrical} \\ \mathbf{conductivity} \text{ (} \mathbf{J} = \sigma \mathbf{E} \text{)}$$



In a cylindrical circuit element with a constant cross section  $S$  and a length  $l$ , and  $\rho$  uniform in the whole volume, the element resistance for a current flowing parallelly to the cylinder axis is:

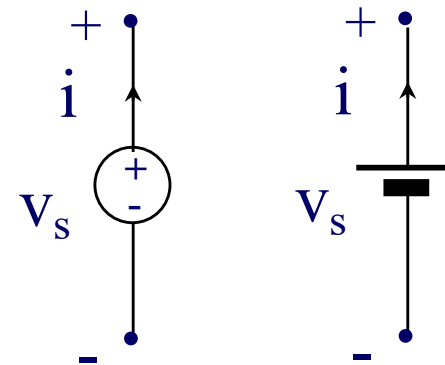
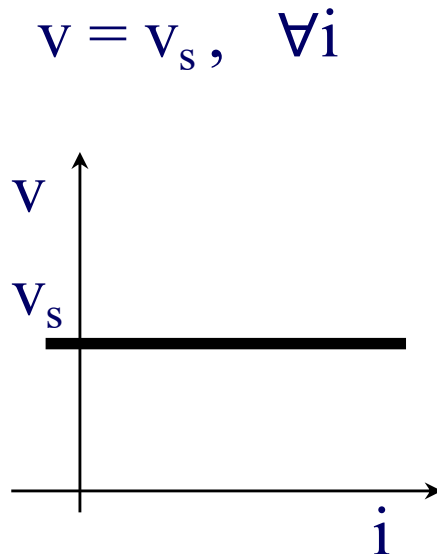
$$R = \rho \frac{l}{S} = \frac{l}{\sigma S}$$



# Elementary Ideal Dipoles

## The Ideal Voltage Source

The **ideal voltage source** is an active element. It keeps the tension  $V_s$  between its terminals independently from the current flowing through it.



*The symbol at the right hand side is used for DC voltages.*

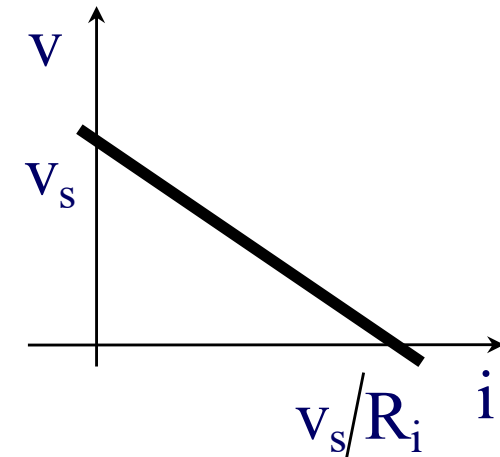
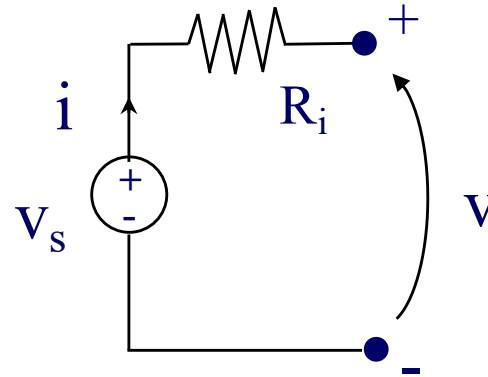




# Real Voltage Source

To simulate a real voltage source a resistor  $R_i$  in series with the ideal source is considered.

$$v = v_s - R_i i$$

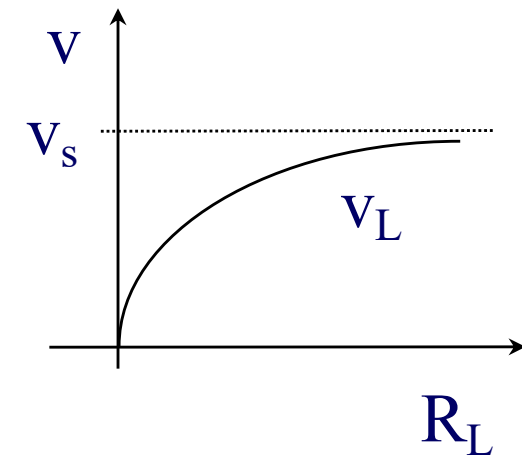
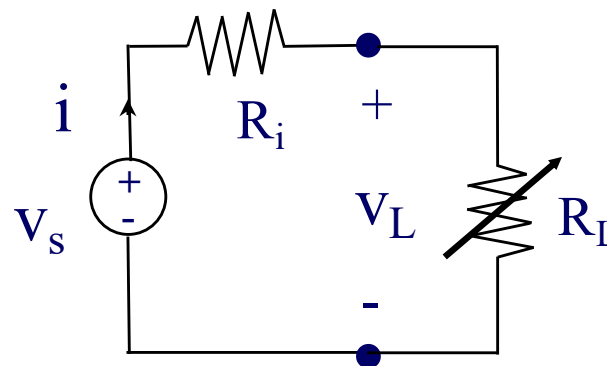


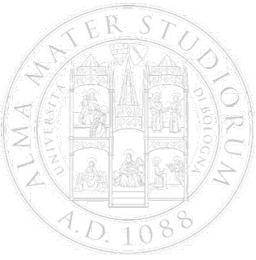
When a variable load  $R_L$  connected to a real source, from the KTL it follows:

$$R_L i + R_i i - v_s = 0$$

$$R_L i - v_L = 0$$

$$\Rightarrow v_L = \frac{R_L}{R_L + R_i} v_s$$



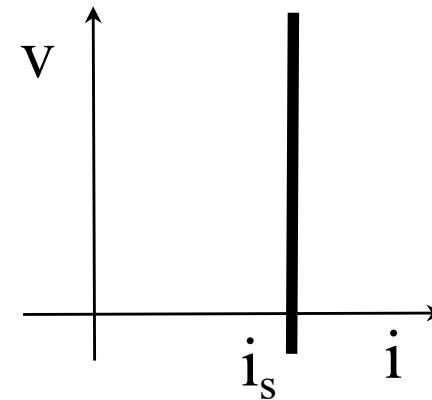
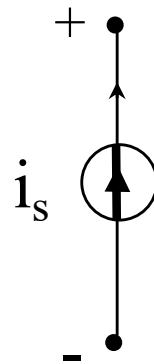


# Elementary Ideal Dipoles

## The Independent Ideal Current Source

The *independent ideal current source* is an active element. It keeps a current  $i_s$  flowing through it independently from the voltage .

$$i = i_s, \quad \forall v$$





# Real Current Source

To simulate a real current source a resistor  $R_i$  in parallel with the ideal source is considered.

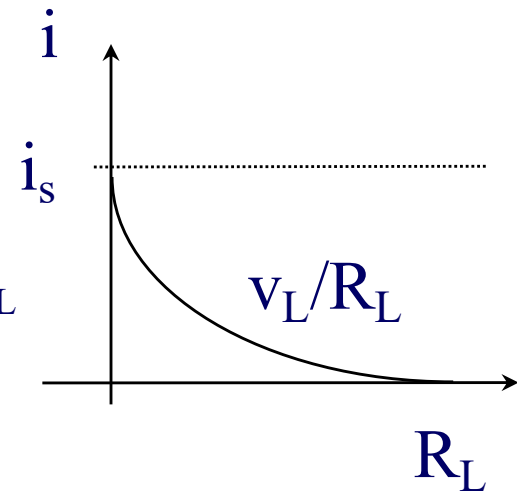
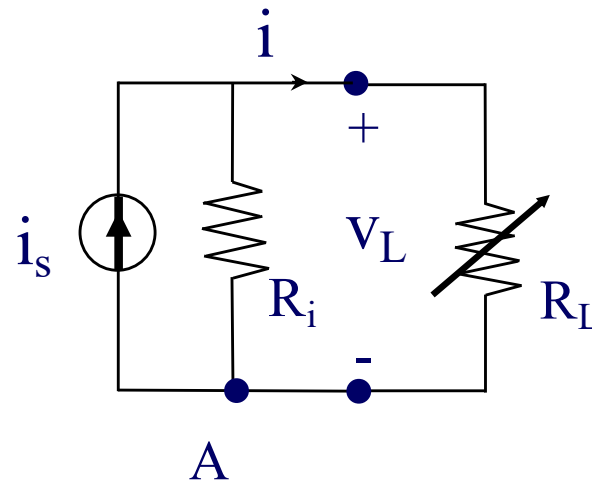
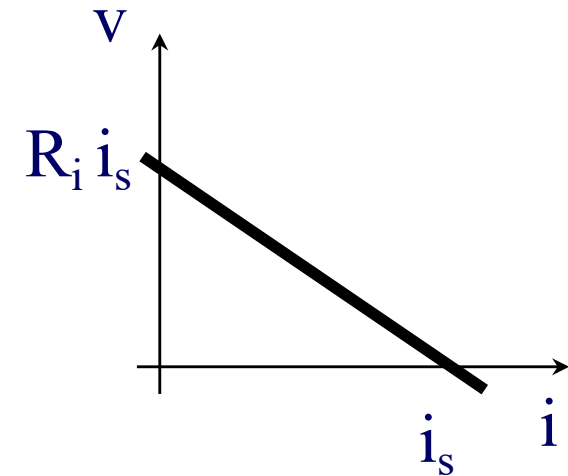
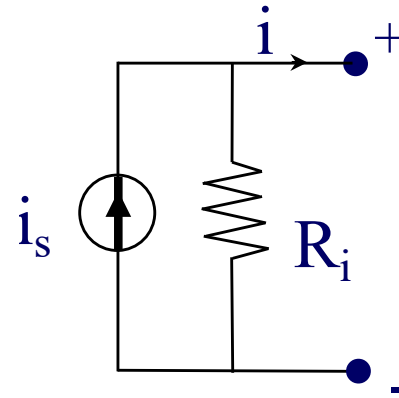
$$i = i_s - v/R_i$$

When a variable load  $R_L$  is connected to a real source, from the KCL it follows :

$$i - i_s + v_L/R_i = 0$$

*and*  $v_L = R_L i$

$$\Rightarrow i = \frac{R_i}{R_L + R_i} i_s$$





# Elementary Ideal Dipoles

## The Capacitor

It consists of two conducting plates separated by an insulating material

$$q = f(v)$$

$$i = \frac{dq}{dt} \Rightarrow i = \frac{d}{dt} f(v)$$

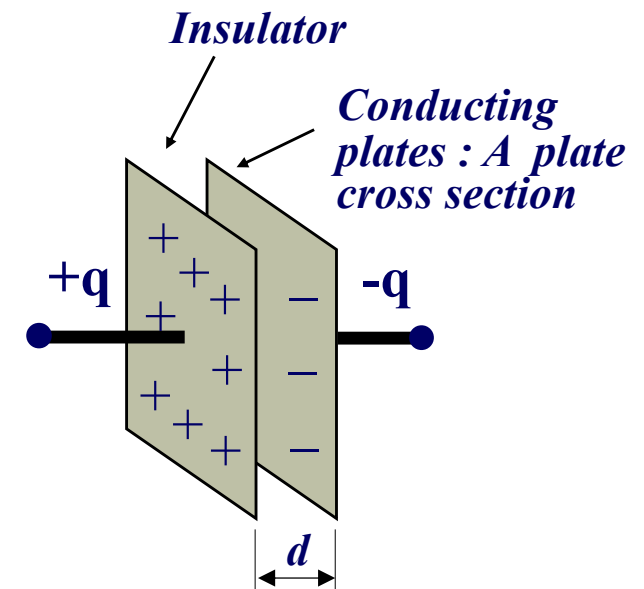
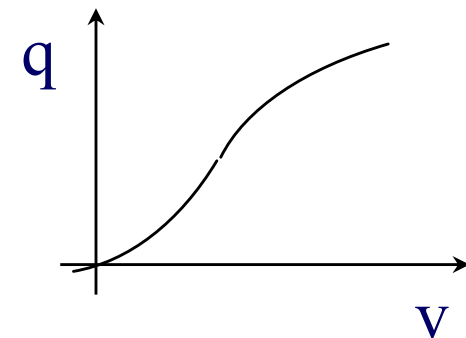
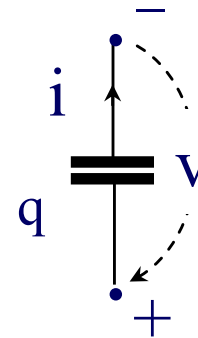
□ For the **linear time independent capacitor** it is:

$$q(t) = C v(t) \Rightarrow i(t) = C \frac{dv(t)}{dt}$$

$C$  is the **capacitance** [SI unit: F (**farad**)]. It is given by the ratio between the absolute value of the charge on one of the capacitor **conducting plates** and the voltage between them. When the insulator (mostly it is a **dielectric**) of a thickness  $d$ , placed between the two armature is homogeneous, it is:

$$C = \epsilon \frac{A}{d}$$

$\epsilon$  is the **dielectric constant** of the insulating material.





# Elementary Ideal Dipoles

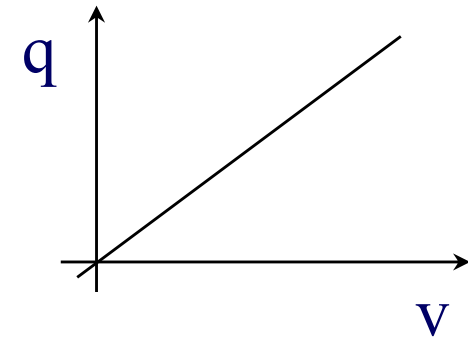
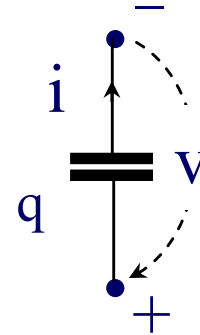
## The linear time independent capacitor

$$q(t) = C v(t),$$

$$dq = C dv \rightarrow i(t) = \frac{dq}{dt} = C \frac{dv}{dt}$$

$$i(t) = C \frac{dv}{dt} \rightarrow dv = \frac{1}{C} i(t) dt \rightarrow v(t) = \frac{1}{C} \int_{-\infty}^t i(t') dt',$$

$$v(t) = v(t_0) + \frac{1}{C} \int_{t_0}^t i(t') dt', \quad \forall t > t_0 \quad [v(-\infty) = 0 \text{ is assumed}]$$



**Energy stored in the capacitor, electrostatic energy, at the time t:**

$$\varepsilon_C = \int_{-\infty}^t v(t') i(t') dt' = \int_0^{q(t)} v(q') dq' = \frac{1}{C} \int_0^{q(t)} q' dq' = \frac{1}{2} \frac{q(t)^2}{C} = \frac{1}{2} C v(t)^2$$

*[At  $t = -\infty$   $q$  is assumed to be zero]*

# The Linear Capacitor

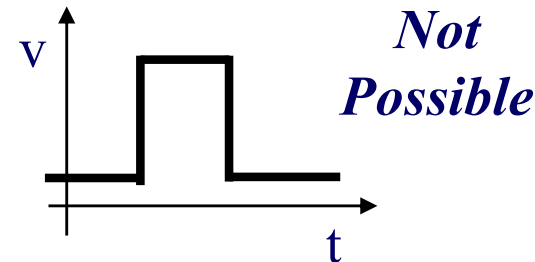
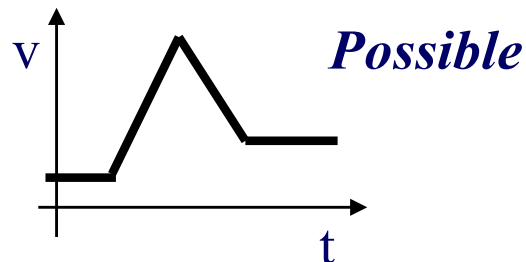
$$q = C v, \quad i = C \frac{dv}{dt}, \quad \varepsilon_C = \frac{1}{2} \frac{q^2}{C} = \frac{1}{2} C v^2$$

□ **For a DC current a capacitor is an open circuit (resistor with  $R = +\infty$ ):**

- When the voltage is constant in time, the current is equal to zero ( $i = C dv/dt$ ).
- When a capacitor is connected to a battery it will be charged ( $q = C v$ ).

□ **The tension and the charge of a capacitor cannot vary instantaneously.**

- As from the capacitor  $i = dq/dt = C dv/dt$ . Hence a charge and a voltage discontinuities ( $dv$  and  $dq$  finite,  $dt$  infinitesimal) imply an infinite current. This is not physically possible. **Therefore the capacitor opposes instantaneous charge and voltage variations.**
- As stated by the capacitor energy relation  $\varepsilon_C = q^2/(2C) = Cv^2/2$  any instantaneous variation of the charge and the voltage implies an instantaneous variation of the energy and therefore an infinite power ( $p_C = d\varepsilon_C/dt$ ). This is not physically possible.



□ **The capacitor is a passive element. It does not dissipate energy. The energy is stored in the form of electrostatic energy. The energy is used to create an electric field due to the deposition of the charges carried by the current on both conducting plates. This energy is given back when the current changes its direction.**



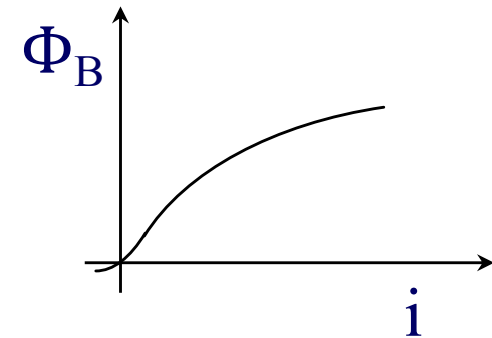
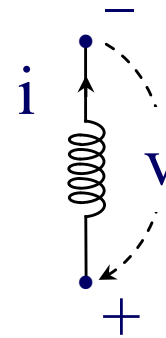
# Elementary Ideal Dipoles

## The Inductor

It consists of windings around a core of ferromagnetic material. The current flows through the windings and generates a magnetic flux. A time variation of this flux induces a voltage.

$$\Phi_B = f(i)$$

$$v = \frac{d\Phi_B}{dt} \Rightarrow v = \frac{d}{dt} f(i)$$



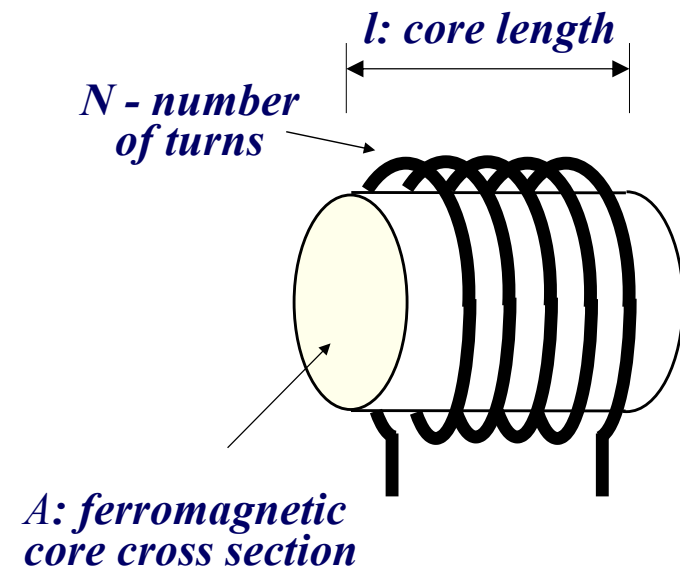
□ For the **linear time independent inductor** it is:

$$\Phi_B(t) = L i(t) \Rightarrow v = L \frac{di}{dt}$$

$L$  is the **inductance** [SI unit: H (**henry**)]. It is given by the ratio between the magnetic flux generated by the current and the current. In an inductor as in the figure with homogeneous material,  $L$  is given by:

$$L = \mu \frac{N^2 A}{l}$$

$\mu$  is the magnetic permeability [SI unit: H/m].





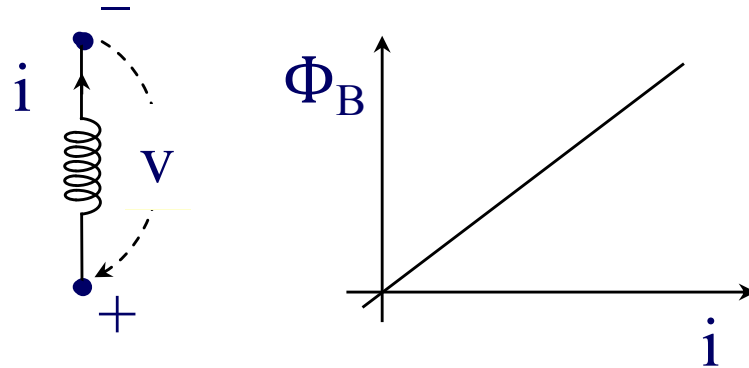


# Elementary Ideal Dipoles

## The linear time independent inductor

$$\Phi_B(t) = L i(t)$$

$$v(t) = \frac{d\Phi_B}{dt} = L \frac{di}{dt}$$

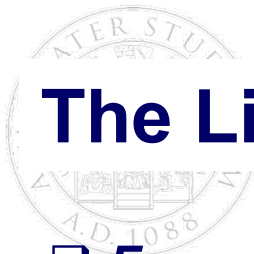


$$v(t) = L \frac{di}{dt} \rightarrow di = \frac{1}{L} v(t) dt \rightarrow i(t) = \frac{1}{L} \int_{-\infty}^t v(t') dt',$$

$$i(t) = i(t_0) + \frac{1}{L} \int_{t_0}^t v(t') dt', \quad \forall t > t_0 \quad [i(-\infty) = 0 \text{ is assumed}]$$

**Energy stored by the inductor – electromagnetic energy - at the time  $t$ :**

$$\mathcal{E}_L = \int_{-\infty}^t v(t') i(t') dt' = \int_0^i L i' di' = \frac{1}{2} L i^2 \quad \leftarrow [i(-\infty) = 0 \text{ is assumed}]$$



# The Linear Inductor

$$\Phi_B = L i, \quad v = L \frac{di}{dt}, \quad \varepsilon_L = \frac{1}{2} L i^2$$

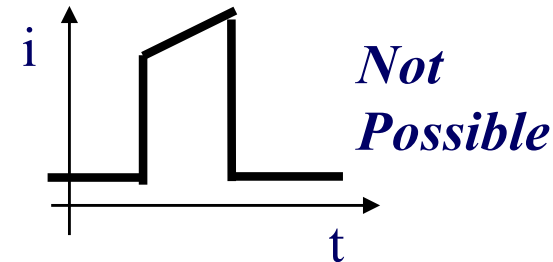
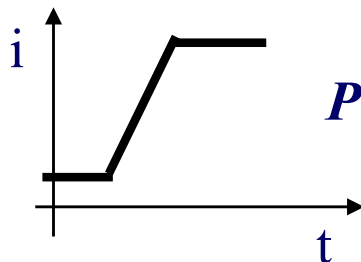
□ **For a DC current an inductor is a closed circuit (Resistor with  $R = 0$ ):**

➤ When the current is constant in time, the voltage is equal to zero ( $v = L di/dt$ ).

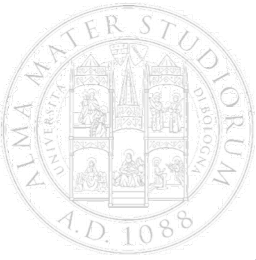
□ **The current in an inductor *cannot* vary instantaneously:**

➤ Due to the inductor equation  $v = L di/dt$ , a current discontinuity implies an infinite tension, that is not physically possible. **Therefore the inductor opposes any sharp variation of the current.**

➤ From the inductor energy relation, any instantaneous variation of the current implies an instantaneous variation of the energy stored into it. Hence in order to have this variation an infinite power ( $p_L = d\varepsilon_L/dt$ ) is necessary. This is not physically possible.



□ **The inductor is a passive element. It does not dissipate energy. The energy is stored in the form of magnetic energy. The energy is used for the creation of the magnetic field by means of the current flowing in the windings. This energy is given back when the current changes its direction.**



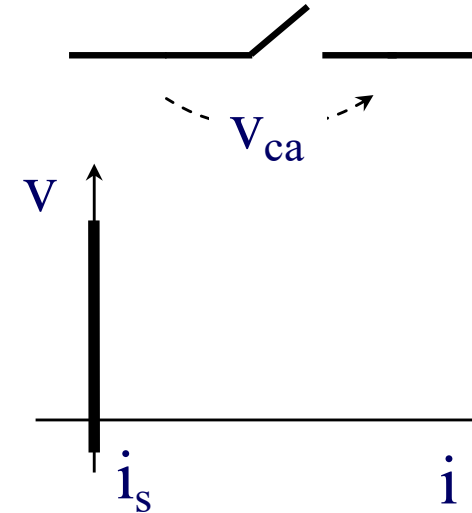
# Two Terminal Elements

## Open Circuit

It can be considered as either of the following elements:

- a current source with  $i_s = 0$
- a resistor with  $R = \infty$

**Element eq.:**  $i = 0, \quad \forall v$

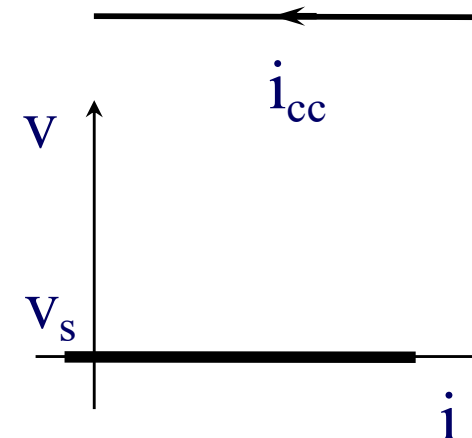


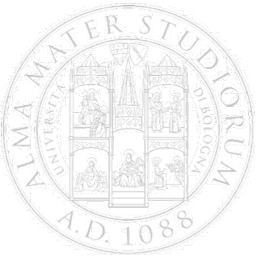
## Closed Circuit

It can be considered as either of the following elements:

- a voltage source with  $v_s = 0$
- a resistor with  $R = 0$

**Element eq.:**  $v = 0, \quad \forall i$

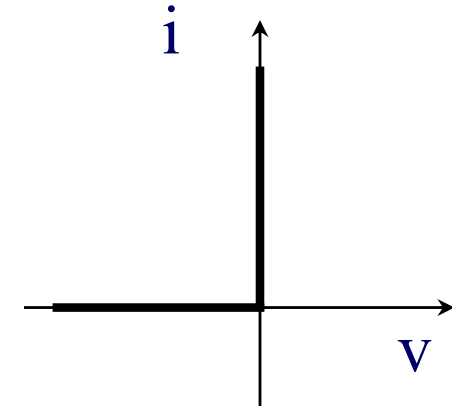
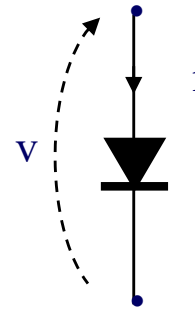




# Two Terminal Elements

## Ideal Diode

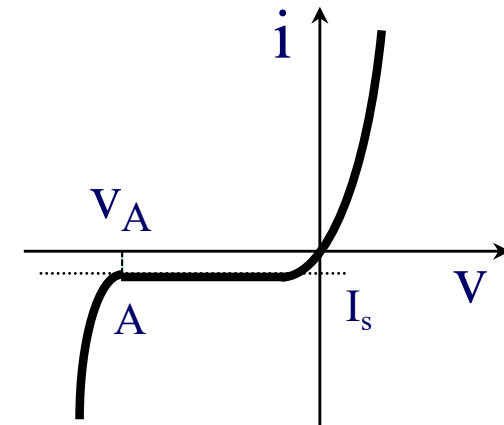
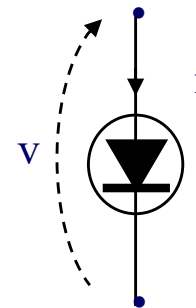
$$v \, i = 0 \iff \begin{cases} i = 0 & \text{per } v < 0 \\ v = 0 & \text{per } i > 0 \end{cases}$$



## pn-Junction Diode

The operation region is for  $v > v_A$ . For  $v < v_A$  the diode burns out.

$$i = I_s \left[ \exp\left(\frac{v}{V_T}\right) - 1 \right]$$



$I_s$  ( $\approx \mu\text{A}$ ) *saturation current*

$V_T = kT/e$  ( $\approx 0.026 \text{ V}$ ) *thermal tension*



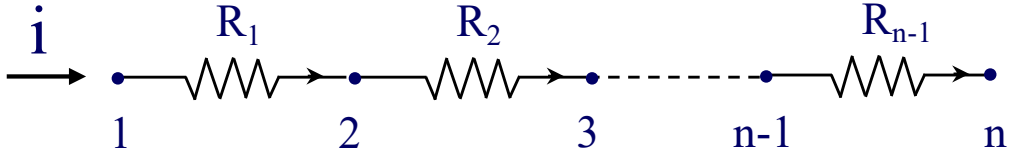
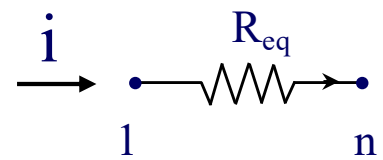
# Series Resistors

Two or more dipoles are said to be ***in series*** when the current from one dipole exclusively flows into the next one. Therefore the same current flows through each element one after another. For a series of resistors it is:

$$\begin{aligned} V_{12} &= R_1 i \\ V_{23} &= R_2 i \\ &\dots\dots\dots \\ V_{n-1,n} &= R_{n-1} i \end{aligned}$$

---

$$V_{1,n} = i \sum_{k=1}^{n-1} R_k \quad \Rightarrow \quad \boxed{\begin{aligned} R_{eq} &= \sum_{k=1}^{n-1} R_k \\ \frac{1}{G_{eq}} &= \sum_{k=1}^{n-1} \frac{1}{G_k} \end{aligned}}$$

  
  
 $(G_k = R_k^{-1}, \quad G_{eq} = R_{eq}^{-1})$



# Parallel Resistors

Two or more dipoles are said to be *in parallel* when they share the same two terminals. Therefore the dipoles will be under the same voltage. For resistors in parallel it is:

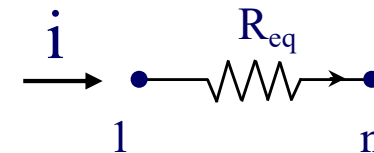
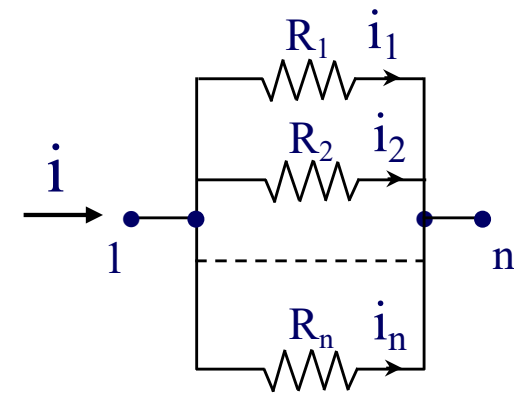
$$\begin{aligned} i &= i_1 + i_2 + \dots + i_n = \\ &= \frac{V_{1,n}}{R_1} + \frac{V_{1,n}}{R_2} + \dots + \frac{V_{1,n}}{R_n} = \\ &= V_{1,n} \left( \frac{1}{R_1} + \frac{1}{R_2} + \dots + \frac{1}{R_n} \right) = \frac{V_{1,n}}{R_{eq}} \end{aligned}$$

$$i = \frac{1}{R_{eq}} V_{1,n}$$

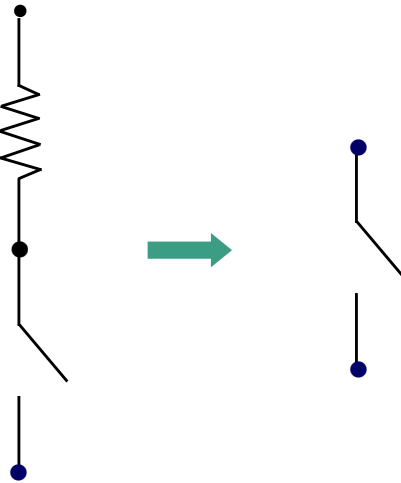
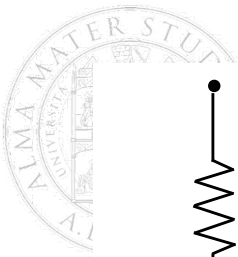


$$\frac{1}{R_{eq}} = \sum_k \frac{1}{R_k}$$

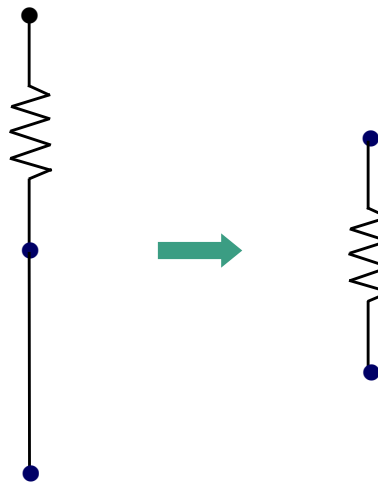
$$G_{eq} = \sum_K G_k$$



$$(G_k = R_k^{-1}, G_{eq} = R_{eq}^{-1})$$



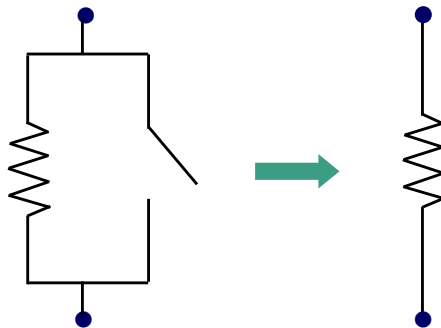
*Resistor in series with an open circuit*



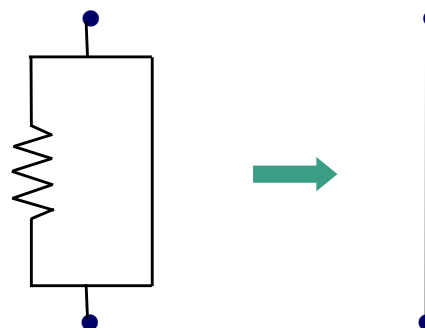
*Resistor in series with a closed circuit*

$$R_{eq} = n R$$

*$n$  resistors in series with equal  $R$*



*Resistor in parallel with an open circuit*

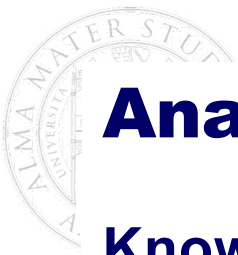


*Resistor in parallel with a closed circuit*

$$R_{eq} = R/n$$

*$n$  resistors in parallel with equal  $R$*





# Analysis of a circuit

## Known quantities:

$$\begin{aligned} R_1 &= 1 \, \Omega, & R_2 &= 1 \, \Omega, & R_3 &= 3 \, \Omega, \\ R_4 &= 2 \, \Omega, & R_5 &= 2 \, \Omega, & R_6 &= 0.5 \, \Omega, \\ R_7 &= 1 \, \Omega, & R_8 &= 6 \, \Omega, & R_9 &= 1 \, \Omega, \\ R_{10} &= 1 \, \Omega. \end{aligned}$$

$$V_s = 40 \, \text{V}.$$

Determine the branch currents.

$$v_{1eq} = i_{1eq} R_{1eq} = 3.33 \, \text{V}$$

$$i_4 = v_{1eq} / R_4; \quad i_5 = v_{1eq} / R_5$$

$$i_4 = 1.67 \, \text{A}$$

$$i_5 = 1.67 \, \text{A}$$

$$i_3 = v_{3eq} / R_3 = -3.33 \, \text{A}$$

$$i_1 = i_2 = -i_{1eq} = v_3 / R_{2eq}$$

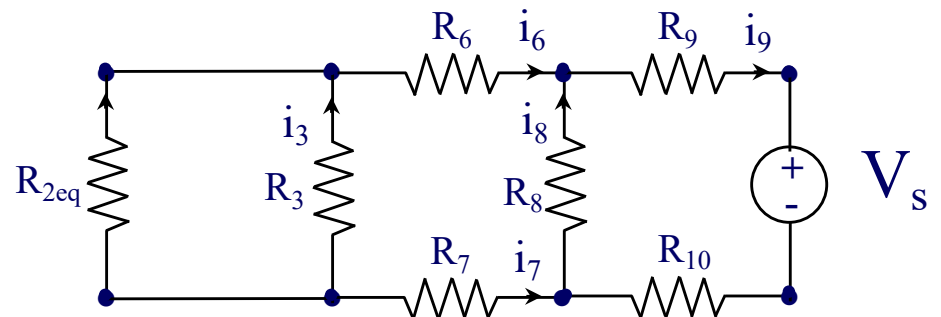
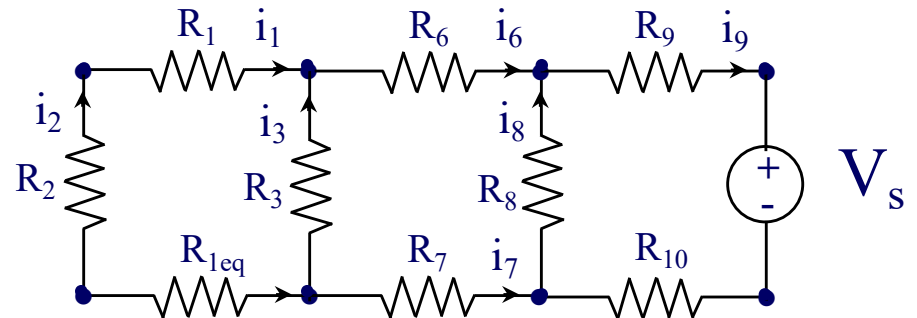
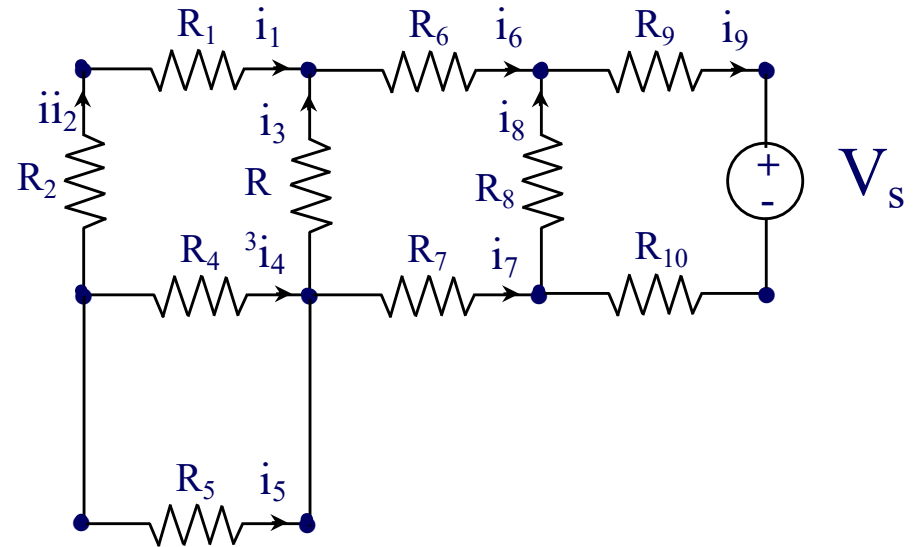
$$i_1 = i_2 = -i_{1eq} = -3.33 \, \text{A}$$

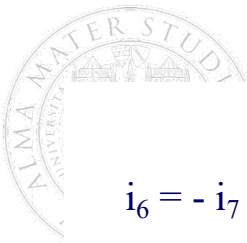
$$R_{1eq} = \frac{R_4 R_5}{R_4 + R_5}$$

$$R_{1eq} = 1 \, \Omega$$

$$R_{2eq} = R_1 + R_2 + R_{1eq}$$

$$R_{2eq} = 3 \, \Omega$$





$$i_6 = -i_7 = i_{4eq} = -6.67 \text{ A}$$

$$v_{4eq} = v_{3eq} + v_6 - v_7$$

$$v_{3eq} = v_{4eq} - R_6 i_6 + R_7 i_7$$

$$v_{3eq} = -10 \text{ V}$$

$$v_{5eq} = v_{4eq} = v_8 = -20 \text{ V}$$

$$i_{4eq} = v_{4eq} / R_{4eq}; i_8 = v_8 / R_8$$

$$i_{4eq} = -6.67 \text{ A};$$

$$i_8 = -3.33 \text{ A}$$

$$v_9 + v_{5eq} + v_{10} = -V_s$$

$$v_{5eq} + R_9 i_9 + R_{10} i_9 = -V_s$$

$$v_{5eq} = -20 \text{ V}$$

$$i_9 = -V_s / R_{Eq} = -10 \text{ A}$$

$$R_{3eq} = \frac{R_{2eq} R_3}{R_{2eq} + R_3}$$

$$R_{3eq} = 1.5 \Omega$$

$$R_{4eq} = R_6 + R_7 + R_{3eq}$$

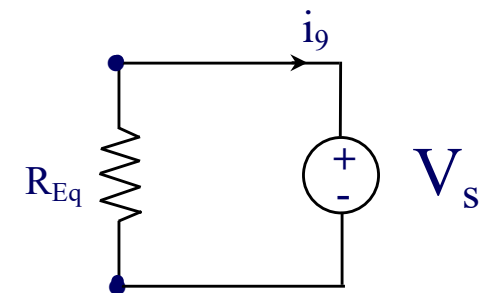
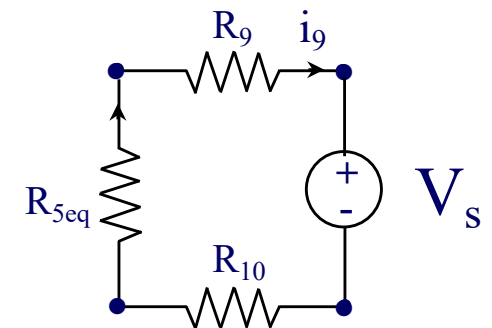
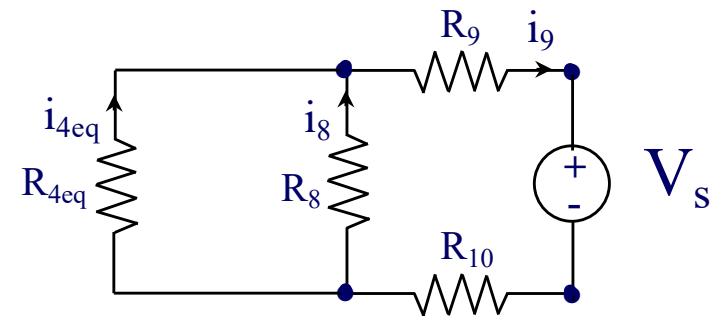
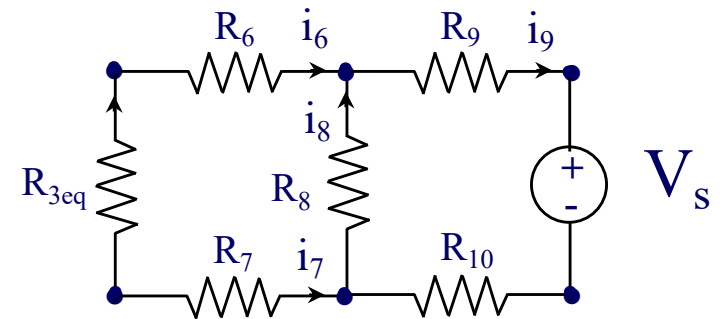
$$R_{4eq} = 3 \Omega$$

$$R_{5eq} = \frac{R_{4eq} R_8}{R_{4eq} + R_8}$$

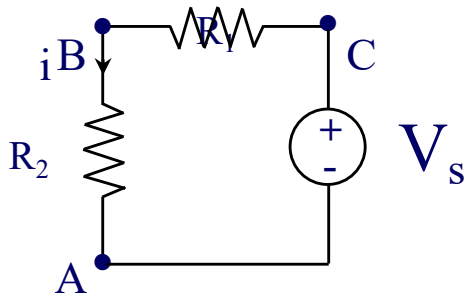
$$R_{5eq} = 2 \Omega$$

$$R_{Eq} = R_9 + R_{10} + R_{5eq}$$

$$R_{Eq} = 4 \Omega$$



## Tension Divider



$$R_{eq} = R_1 + R_2$$

$$i = \frac{V_s}{R_{eq}} = \frac{V_s}{R_1 + R_2}$$

From the KTL it is

$$v_1 + v_2 - V_s = 0$$

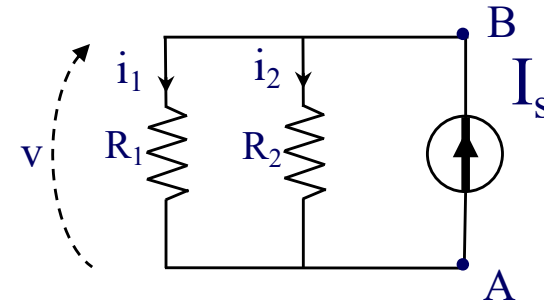
$$\Rightarrow \begin{aligned} v_1 &= V_s - v_2 = V_s - R_2 i \\ v_2 &= V_s - v_1 = V_s - R_1 i \end{aligned}$$

$$v_1 = \frac{R_1}{R_1 + R_2} V_s$$

$$v_2 = \frac{R_2}{R_1 + R_2} V_s$$

*As  $p = V_s^2 / R_{eq}$ , to reduce the power dissipated,  $R_{eq}$  has to be large.*

## Current Divider



$$R_{eq} = \frac{R_1 R_2}{R_1 + R_2}$$

$$v = R_{eq} I_s = \frac{R_1 R_2}{R_1 + R_2} I_s$$

From the KCL it is

$$i_1 + i_2 - I_s = 0$$

$$\Rightarrow \begin{aligned} i_1 &= I_s - i_2 = I_s - \frac{v}{R_2} \\ i_2 &= I_s - i_1 = I_s - \frac{v}{R_1} \end{aligned}$$

$$i_1 = \frac{R_2}{R_1 + R_2} I_s$$

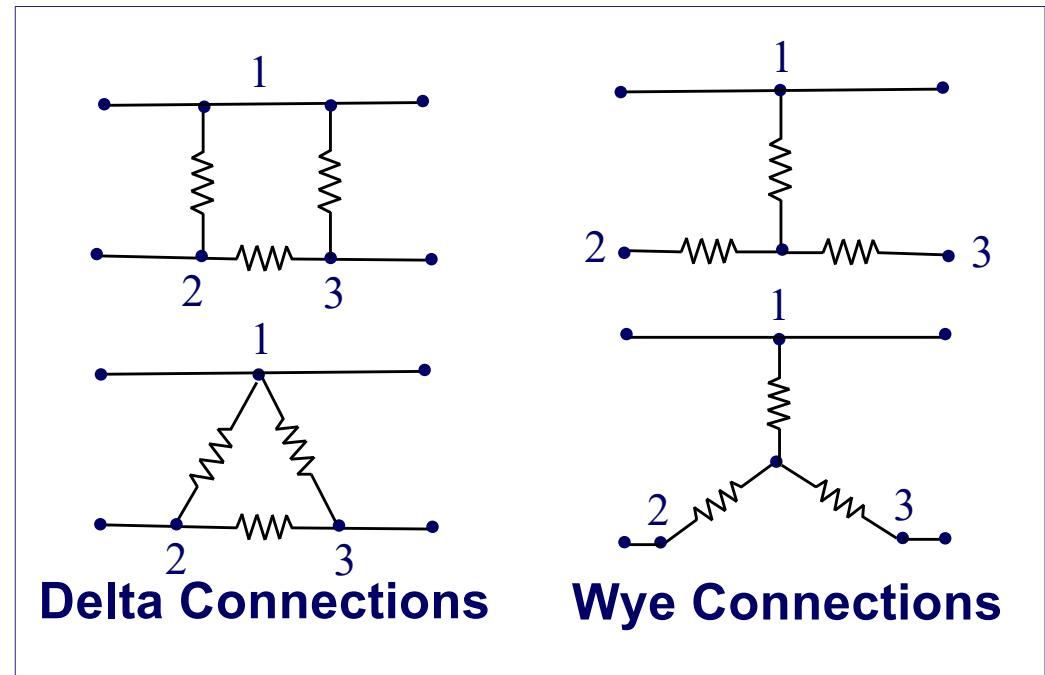
$$i_2 = \frac{R_1}{R_1 + R_2} I_s$$

*As  $p = R_{eq} I_s^2$ , to reduce the power dissipated,  $R_{eq}$  has to be low.*

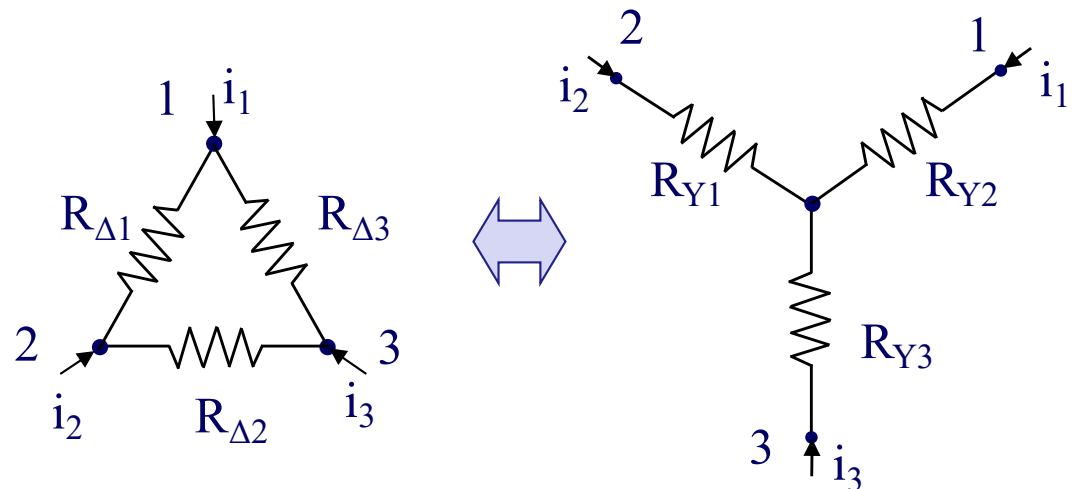


# Wye and Delta Resistor Connections

A system of three resistances may be delta connected or wye connected. It can be more convenient to work with a wye network in a place where the circuit contains a delta configuration. **A wye network can operate in an equivalent way as a delta network and in the other way around.**



This means that the same tensions  $v_{12}$ ,  $v_{23}$  and  $v_{31}$  between nodes 1 and 2, nodes 2 and 3, and nodes 3 and 1 induce the same currents to node 1, node 2 and node 3.



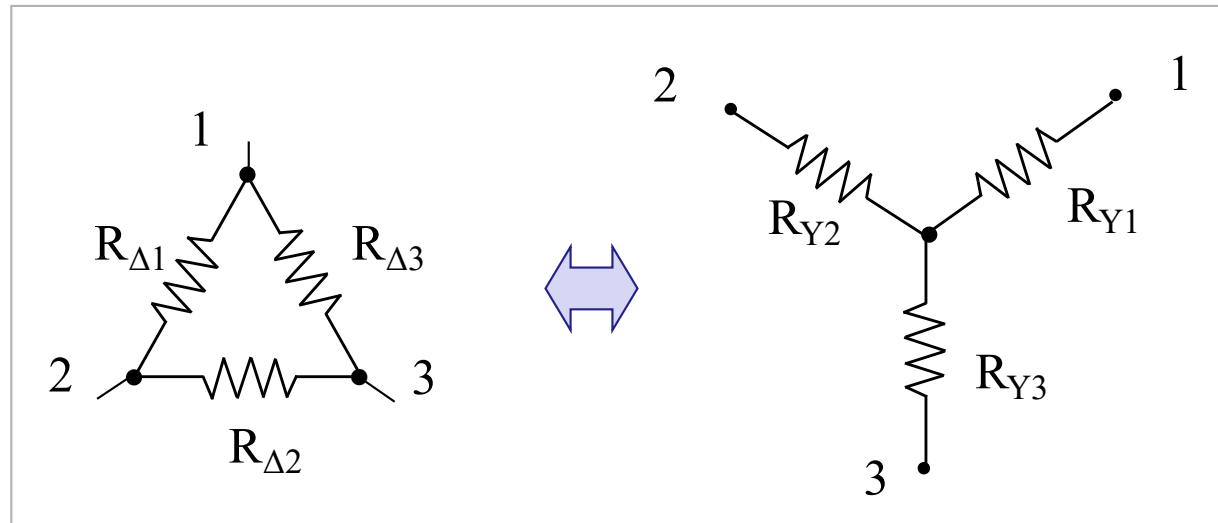


# Wye and Delta Resistor Connections

Between node 1 and node 2, if node 3 is not connected, in the **wye** and the **delta** connection there are the following resistances

$$R_{12}(Y) = R_{Y1} + R_{Y2}$$

$$R_{12}(\Delta) = R_{\Delta1} // (R_{\Delta2} + R_{\Delta3})$$



If node 3 is not connected the same current has to correspond to the same voltage. This has to hold for the branches 1-3 and 2-3 when node 2 and are not connected. Therefore :

$$R_{12}(Y) = R_{12}(\Delta)$$

$$R_{13}(Y) = R_{13}(\Delta)$$

$$R_{23}(Y) = R_{23}(\Delta)$$



$$R_{Y1} + R_{Y2} = \frac{R_{\Delta1} (R_{\Delta2} + R_{\Delta3})}{R_{\Delta1} + R_{\Delta2} + R_{\Delta3}}$$

$$R_{Y1} + R_{Y3} = \frac{R_{\Delta3} (R_{\Delta1} + R_{\Delta2})}{R_{\Delta1} + R_{\Delta2} + R_{\Delta3}}$$

$$R_{Y2} + R_{Y3} = \frac{R_{\Delta2} (R_{\Delta1} + R_{\Delta3})}{R_{\Delta1} + R_{\Delta2} + R_{\Delta3}}$$

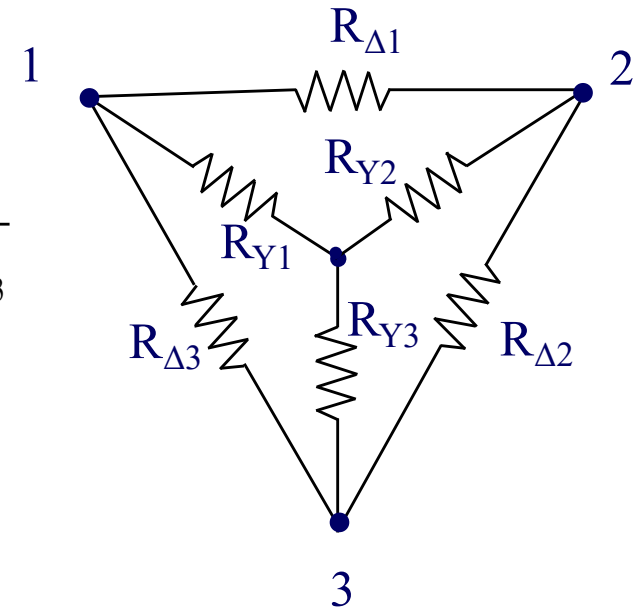




# Y-Δ Transformation

$$R_{Y1} = \frac{R_{\Delta1} R_{\Delta3}}{R_{\Delta1} + R_{\Delta2} + R_{\Delta3}}; R_{Y2} = \frac{R_{\Delta1} R_{\Delta2}}{R_{\Delta1} + R_{\Delta2} + R_{\Delta3}}; R_{Y3} = \frac{R_{\Delta2} R_{\Delta3}}{R_{\Delta1} + R_{\Delta2} + R_{\Delta3}}$$

Each resistance of the **wye** connection is the product of the two resistance of the **delta** connection connected to the same node, divided by the sum of the three resistances of the delta connection.



$$R_{\Delta1} = \frac{R_{Y1} R_{Y2} + R_{Y2} R_{Y3} + R_{Y3} R_{Y1}}{R_{Y3}}; R_{\Delta2} = \frac{R_{Y1} R_{Y2} + R_{Y2} R_{Y3} + R_{Y3} R_{Y1}}{R_{Y1}}; R_{\Delta3} = \frac{R_{Y1} R_{Y2} + R_{Y2} R_{Y3} + R_{Y3} R_{Y1}}{R_{Y2}}$$

Each resistance of the **delta** connection is the sum of all the products of the resistances of the **wye** connection two by two, divided by the resistance in the opposite branch of the wye connection.

For  $R_{Y1} = R_{Y2} = R_{Y3} = R_Y$  and  $R_{\Delta1} = R_{\Delta2} = R_{\Delta3} = R_{\Delta}$  it results:

$$R_Y = R_{\Delta}/3 \quad \text{and} \quad R_{\Delta} = 3 R_Y$$

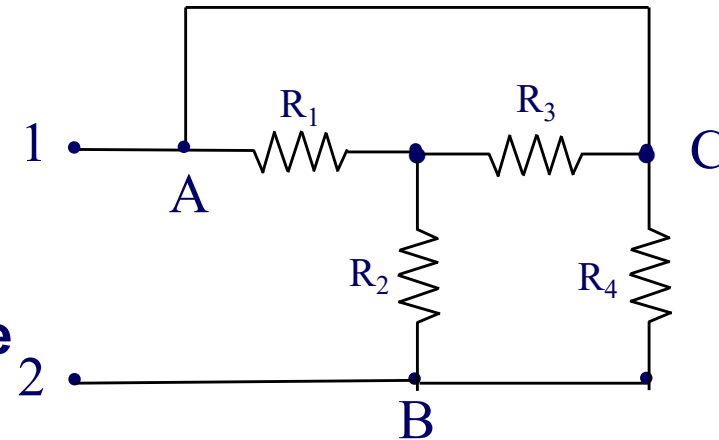


# Y-Δ Transformation

**Known quantities:**

$$R_1 = 3 \, \Omega, \quad R_2 = 3 \, \Omega, \quad R_3 = 3 \, \Omega, \\ R_4 = 2 \, \Omega,$$

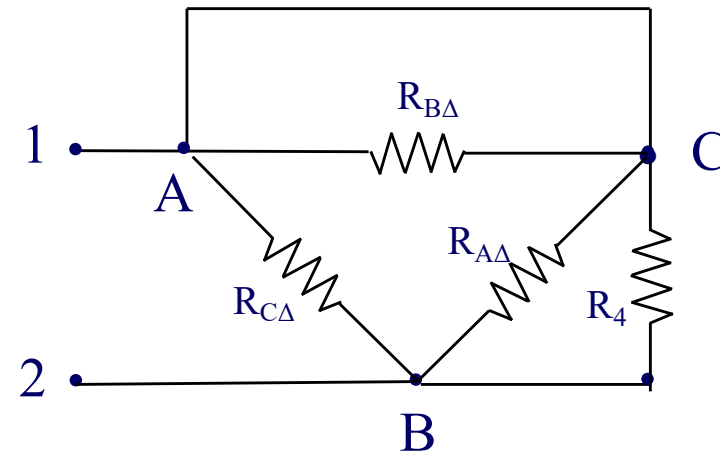
**Determine the equivalent resistance between the nodes 1 and 2.**



$$R_{A\Delta} = \frac{R_1 R_2 + R_2 R_3 + R_1 R_3}{R_1} = 9 \, \Omega$$

$$R_{B\Delta} = \frac{R_1 R_2 + R_2 R_3 + R_1 R_3}{R_2} = 9 \, \Omega$$

$$R_{C\Delta} = \frac{R_1 R_2 + R_2 R_3 + R_1 R_3}{R_3} = 9 \, \Omega$$





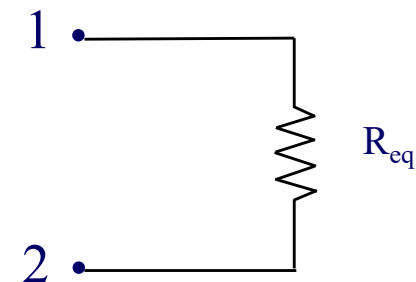
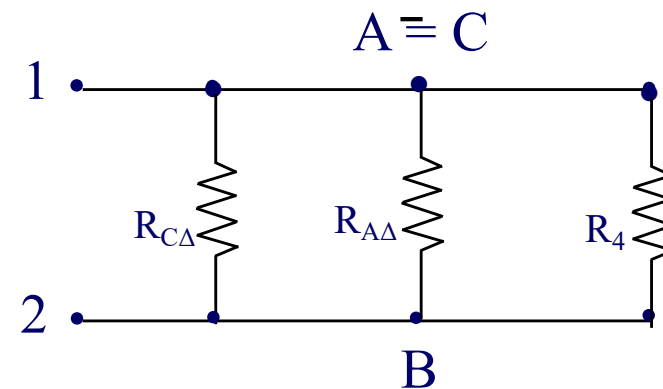
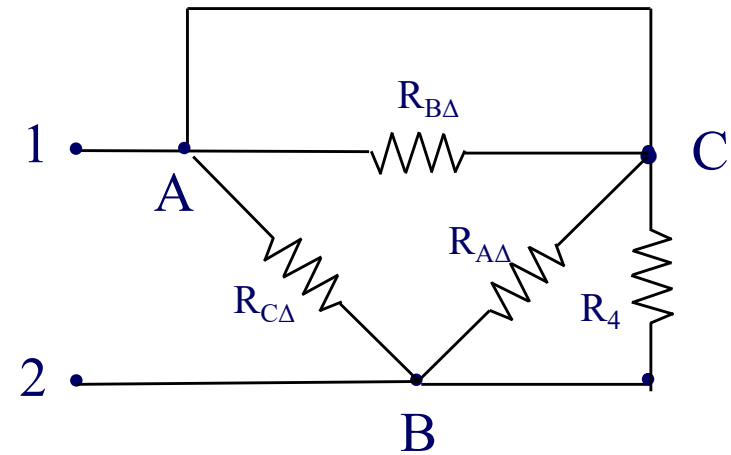
# Y-Δ Transformation

## Known quantities:

$$R_1 = 3 \, \Omega, \quad R_2 = 3 \, \Omega, \quad R_3 = 3 \, \Omega, \\ R_4 = 2 \, \Omega,$$

$$R_{A\Delta} = R_{B\Delta} = R_{C\Delta} = 9 \, \Omega$$

$$R_{eq} = \frac{R_4 R_{A\Delta} R_{C\Delta}}{R_4 R_{A\Delta} + R_{A\Delta} R_{C\Delta} + R_4 R_{C\Delta}} = \\ = 1,385 \, \Omega$$



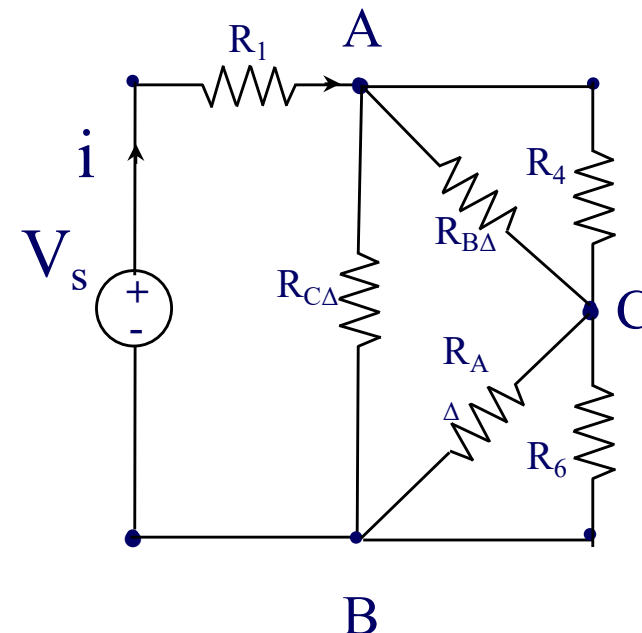
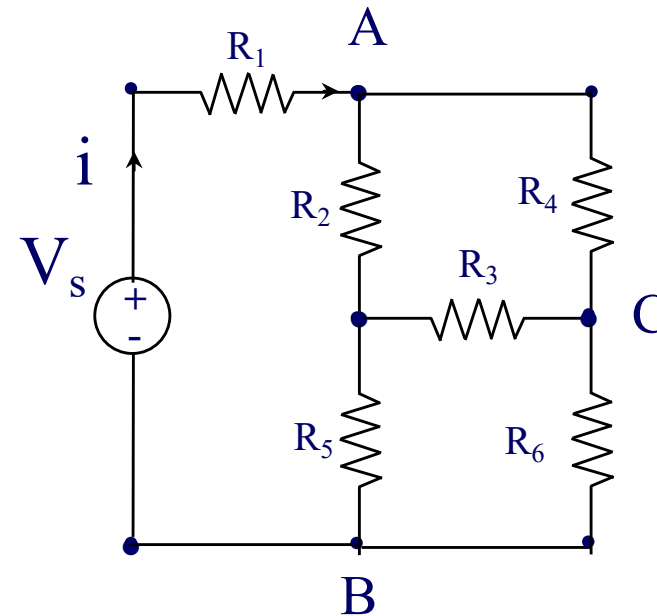


# Y-Δ Transformation

## Known quantities:

$$R_1 = 1 \, \Omega, \quad R_2 = 2 \, \Omega, \quad R_3 = 2 \, \Omega, \\ R_4 = 3 \, \Omega, \quad R_5 = 2 \, \Omega, \quad R_6 = 0.5 \, \Omega, \\ V_s = 40 \, \text{V}.$$

**Determine the power delivered by the voltage generator.**



$$R_{A\Delta} = \frac{R_2 R_5 + R_2 R_3 + R_5 R_3}{R_2} = 6 \, \Omega$$

$$R_{B\Delta} = \frac{R_2 R_5 + R_2 R_3 + R_5 R_3}{R_5} = 6 \, \Omega$$

$$R_{C\Delta} = \frac{R_2 R_5 + R_2 R_3 + R_5 R_3}{R_3} = 6 \, \Omega$$

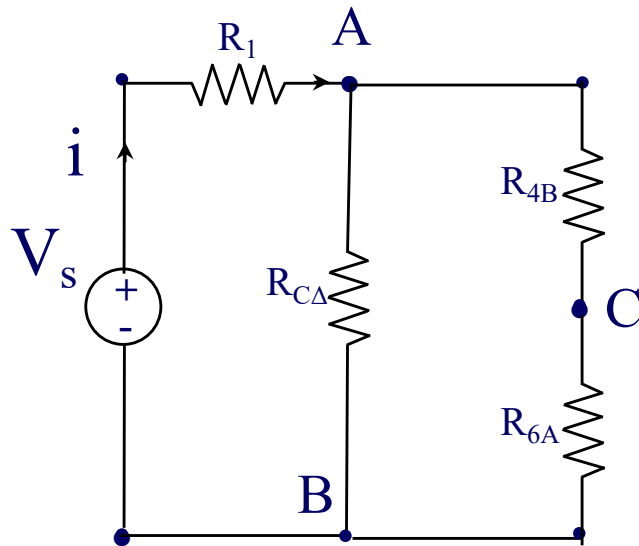
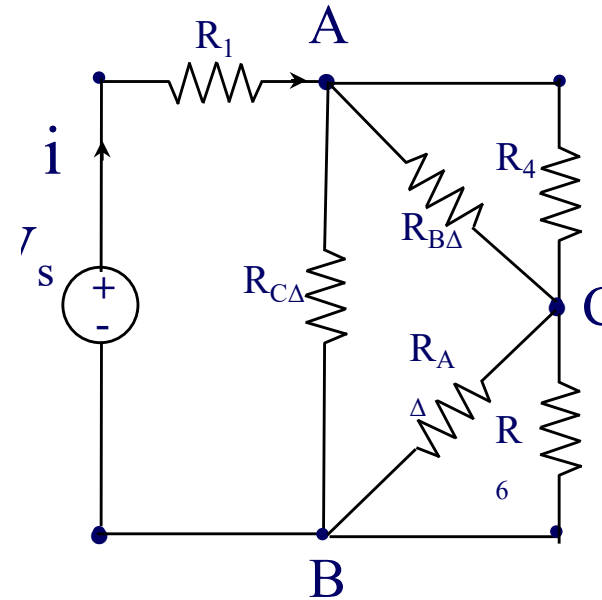


# Y-Δ Transformation

**Known quantities:**

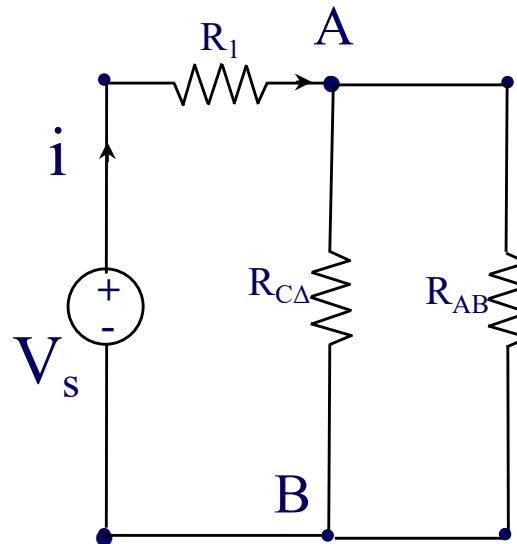
$$R_1 = 1 \, \Omega, \quad R_2 = 2 \, \Omega, \quad R_3 = 2 \, \Omega, \\ R_4 = 3 \, \Omega, \quad R_5 = 2 \, \Omega, \quad R_6 = 0.5 \, \Omega, \\ V_s = 40 \, \text{V}.$$

$$R_{A\Delta} = R_{B\Delta} = R_{C\Delta} = 6 \, \Omega$$



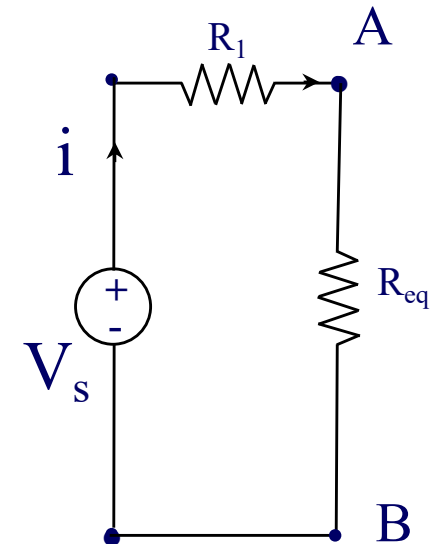
$$R_{4B} = \frac{R_4 R_{6\Delta}}{R_4 + R_{6\Delta}} = 2 \, \Omega$$

$$R_{6A} = \frac{R_6 R_{A\Delta}}{R_6 + R_{A\Delta}} = 0,46 \, \Omega$$



$$R_{AB} = R_{4B} + R_{6A} = 2,46 \, \Omega$$

$$R_{eq} = \frac{R_{C\Delta} R_{AB}}{R_{C\Delta} + R_{AB}} = 1,745 \, \Omega$$



$$i = V_s / (R_1 + R_{eq}) = 14,572 \, \text{A}$$

$$p = i V_s = 582,88 \, \text{W}$$

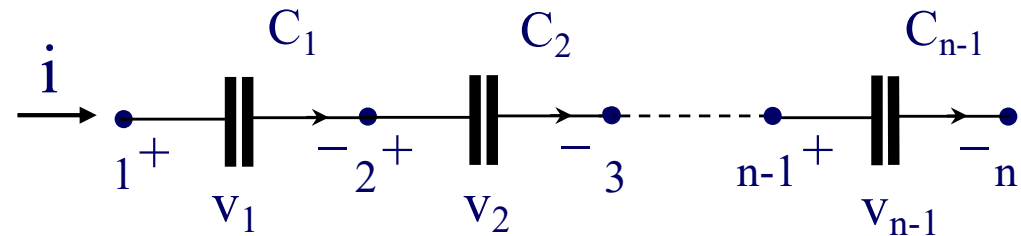


# Capacitors in series

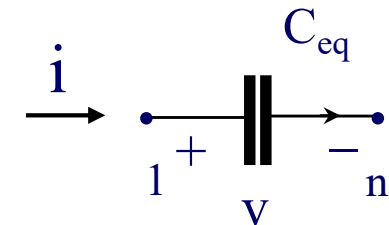
Two or more capacitors are ***in series*** when the current from one element exclusively flows into the next one. Therefore the same current flows through each element one after another:

$$V = V_1 + V_2 + \dots + V_{n-1}$$

$$\text{dove: } v_k = \frac{1}{C_k} \int_{t_0}^t i(t') dt' + v_k(t_0)$$



$$v = \frac{1}{C_1} \int_{t_0}^t i(t') dt' + \frac{1}{C_2} \int_{t_0}^t i(t') dt' + \dots + \frac{1}{C_{n-1}} \int_{t_0}^t i(t') dt' + v_1(t_0) + v_2(t_0) + \dots + v_{n-1}(t_0)$$



$$v = \left( \frac{1}{C_1} + \frac{1}{C_2} + \dots + \frac{1}{C_{n-1}} \right) \int_{t_0}^t i(t') dt' + v_1(t_0) + v_2(t_0) + \dots + v_{n-1}(t_0)$$

$$v = \frac{1}{C_{eq}} \int_{t_0}^t i(t') dt' + v(t_0) \quad \text{dove:}$$

$$\frac{1}{C_{eq}} = \frac{1}{C_1} + \frac{1}{C_2} + \dots + \frac{1}{C_{n-1}}$$



# Capacitors in parallel

Two or more capacitors are *in parallel* when they share the same two terminals. Therefore the elements will be under the same voltage:

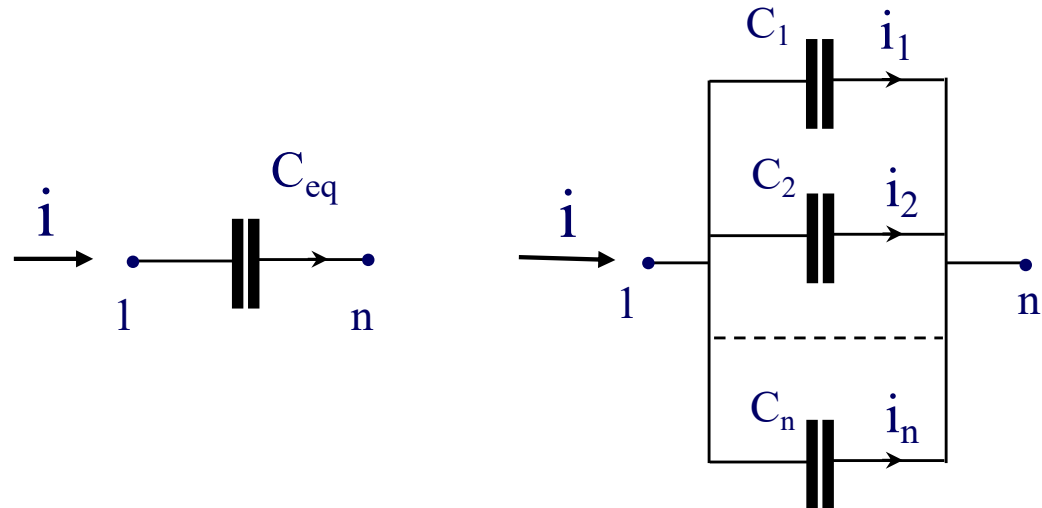
$$i = i_1 + i_2 + \dots + i_n$$

$$\text{dove: } i_k = C_k \frac{dv}{dt}$$

$$i = C_1 \frac{dv}{dt} + C_2 \frac{dv}{dt} + \dots + C_n \frac{dv}{dt}$$

$$i = (C_1 + C_2 + \dots + C_n) \frac{dv}{dt}$$

$$i = C_{eq} \frac{dv}{dt} \quad \text{dove:}$$



$$C_{eq} = C_1 + C_2 + \dots + C_n$$



# Inductors in series

Two or more two inductors are ***in series*** when the current from one element exclusively flows into the next one. Therefore the same current flows through each element one after another:

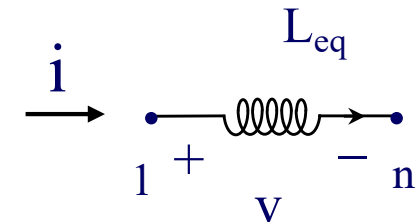
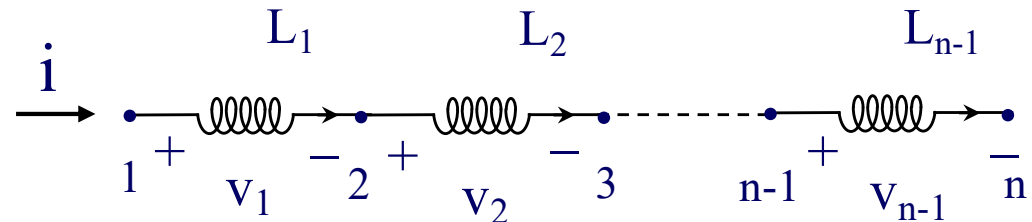
$$v = v_1 + v_2 + \dots + v_{n-1}$$

$$\text{dove : } v_k = L_k \frac{di}{dt}$$

$$v = L_1 \frac{di}{dt} + L_2 \frac{di}{dt} + \dots + L_{n-1} \frac{di}{dt}$$

$$v = (L_1 + L_2 + \dots + L_{n-1}) \frac{di}{dt}$$

$$v = L_{eq} \frac{di}{dt} \quad \text{dove :}$$



$$L_{eq} = L_1 + L_2 + \dots + L_{n-1}$$



# Inductors in parallel

Two or more inductors are *in parallel* when they share the same two terminals. Therefore the elements will be under the same voltage:

$$i = i_1 + i_2 + \dots + i_n$$

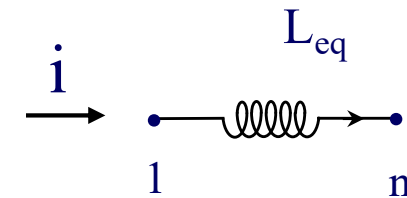
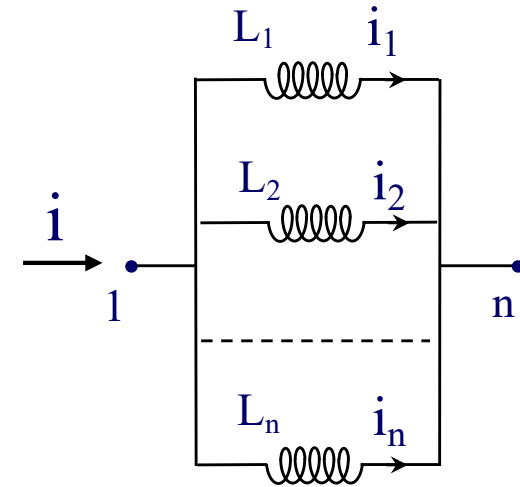
$$\text{dove: } i_k = \frac{1}{L_k} \int_{t_0}^t v(t') dt' + i_k(t_0)$$

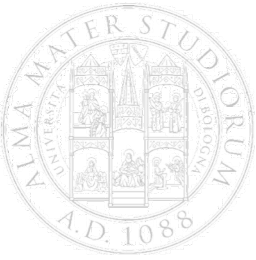
$$i = \frac{1}{L_1} \int_{t_0}^t v(t') dt' + \frac{1}{L_2} \int_{t_0}^t v(t') dt' + \dots + \frac{1}{L_n} \int_{t_0}^t v(t') dt' + i_1(t_0) + i_2(t_0) + \dots + i_n(t_0)$$

$$i = \left( \frac{1}{L_1} + \frac{1}{L_2} + \dots + \frac{1}{L_n} \right) \int_{t_0}^t v(t') dt' + i_1(t_0) + i_2(t_0) + \dots + i_n(t_0)$$

$$i = \frac{1}{L_{eq}} \int_{t_0}^t v(t') dt' + i(t_0) \quad \text{dove:}$$

$$\frac{1}{L_{eq}} = \frac{1}{L_1} + \frac{1}{L_2} + \dots + \frac{1}{L_n}$$





# Magnetic Flux in Inductors

The Magnetic flux  $\Phi_B$ , **linked** with the winding, through which the current  $i$  flows, is **generated** by  $i$  (1° Maxwell's law or Ampere's law):

$$\Phi_B = f(i)$$

For a linear inductor it is

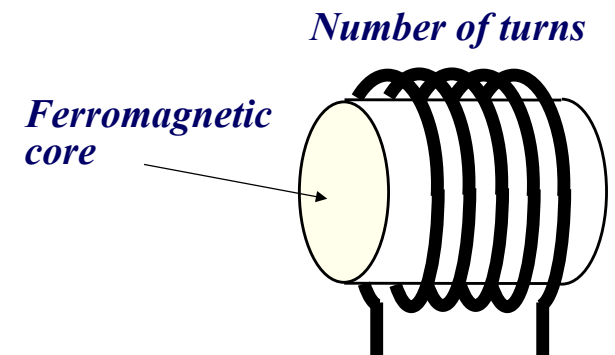
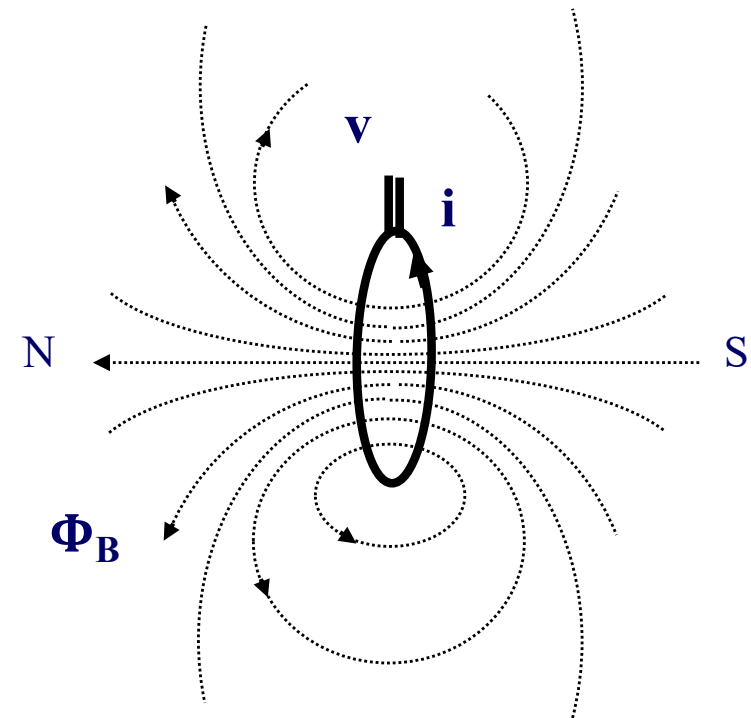
$$\Phi_B = L i$$

For the 2° Maxwell's law (also Faraday's law or inductance law) it is

$$v = \frac{d\Phi_B}{dt}$$



$$v = L \frac{di}{dt}$$



# Linear Time Independent Coupled Inductors

The magnetic flux linked with the circuit 1 is generated by two currents: the flux component generated by  $i_1$  and the flux component generated by  $i_2$ :

$$\Phi_{C1} = \Phi_1(i_1) + \Phi_1(i_2)$$

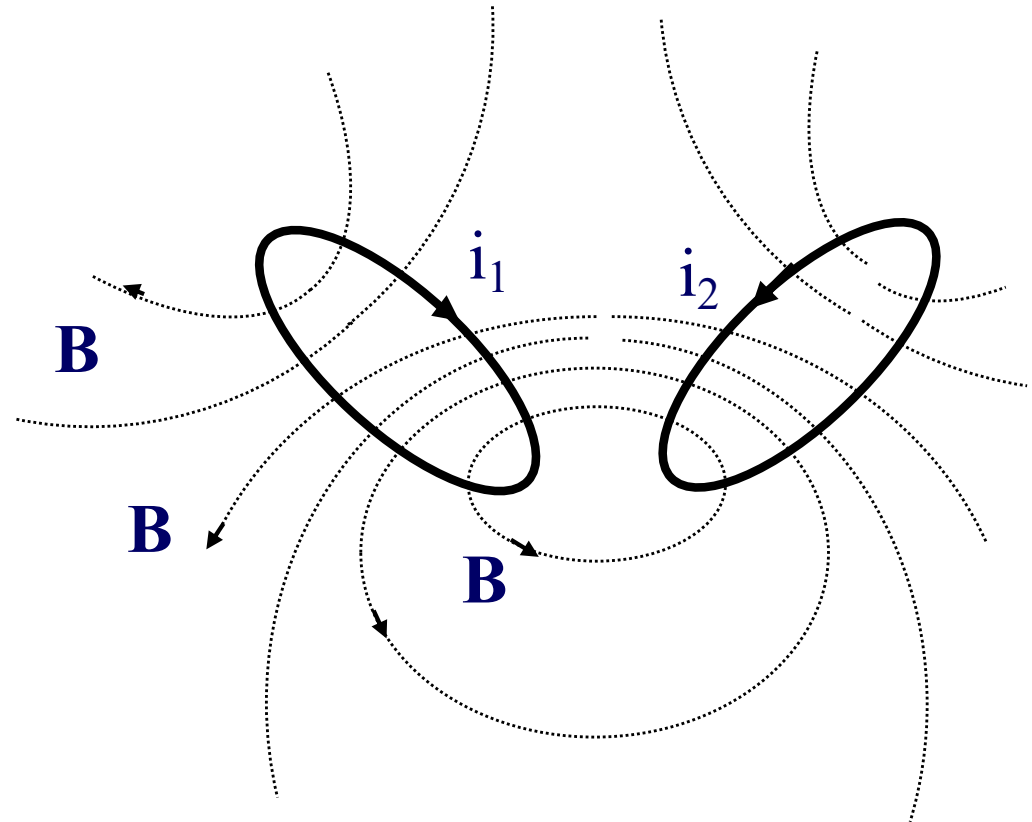
For the linear case it is

$$\Phi_{C1} = L_1 i_1 + M_{12} i_2$$

Here  $L_1$  and  $M_{12}$  are the **self inductance** and the **mutual inductance** respectively.

For time independent circuit elements it results to be

$$v_1 = \frac{d\Phi_{C1}}{dt} = L_1 \frac{di_1}{dt} + M_{12} \frac{di_2}{dt}$$



*$M_{12}$  depends on each of the two circuits and on their relative positions.  $M_{12}$  can be positive or negative.*



# Linear Time Independent Coupled Inductors

□ The two circuits, with or without contacts between them, affect each other by means of the magnetic field generated by the currents flowing through them. They are said to be ***magnetically coupled***.

For two magnetically coupled inductors it is

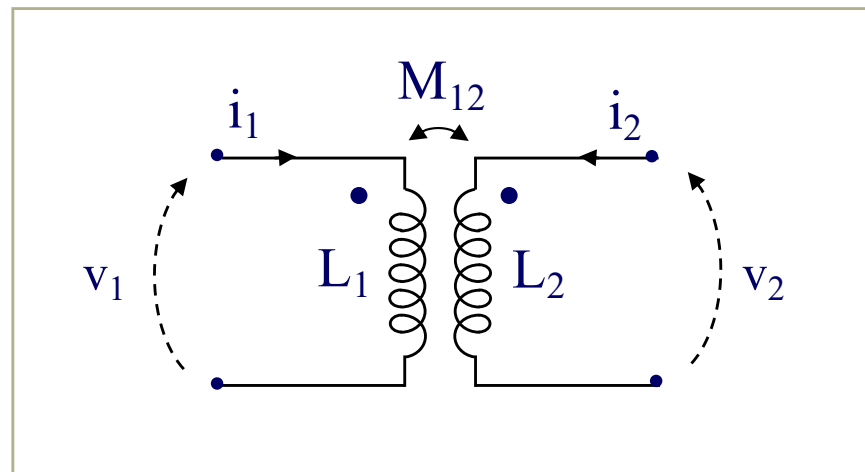
$$\Phi_{C1} = L_1 i_1 + M_{12} i_2$$

$$\Phi_{C2} = L_2 i_2 + M_{21} i_1$$

**It can be demonstrated that  $M_{12} = M_{21}$ .** Hence it results:

$$v_1 = L_1 \frac{di_1}{dt} + M_{12} \frac{di_2}{dt}$$

$$v_2 = M_{12} \frac{di_1}{dt} + L_2 \frac{di_2}{dt}$$

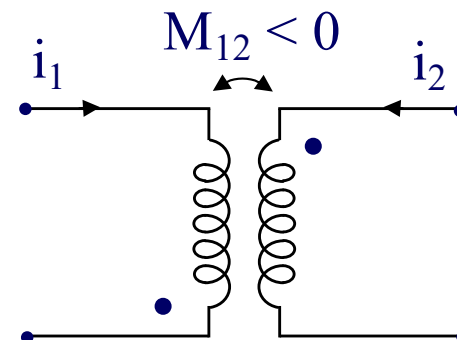
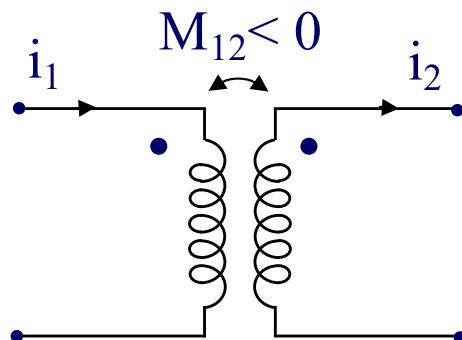
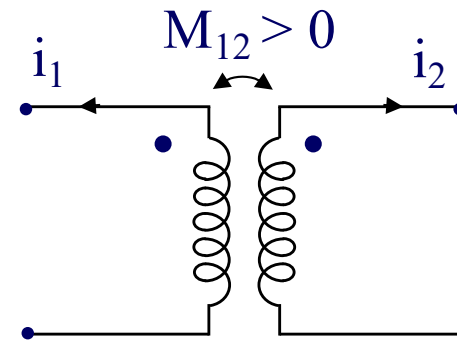
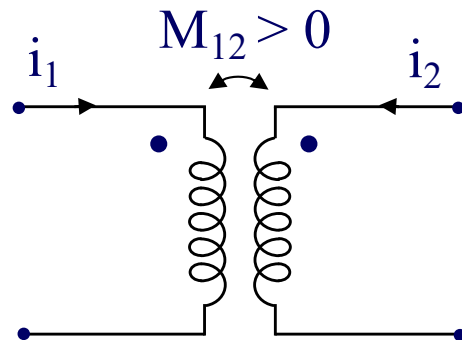


***Spot convention:***  $M_{12} > 0$  when the spots correspond to terminals with currents flowing inside both of them or terminals with currents flowing outside both of them.

# Linear Time Independent Coupled Inductors

## Spot Convention

When the spots correspond to the two terminals with the currents flowing inside both or terminals with currents flowing outside both, the mutual inductance  $M_{12}$  is assumed to be positive. Otherwise  $M_{12}$  is negative.

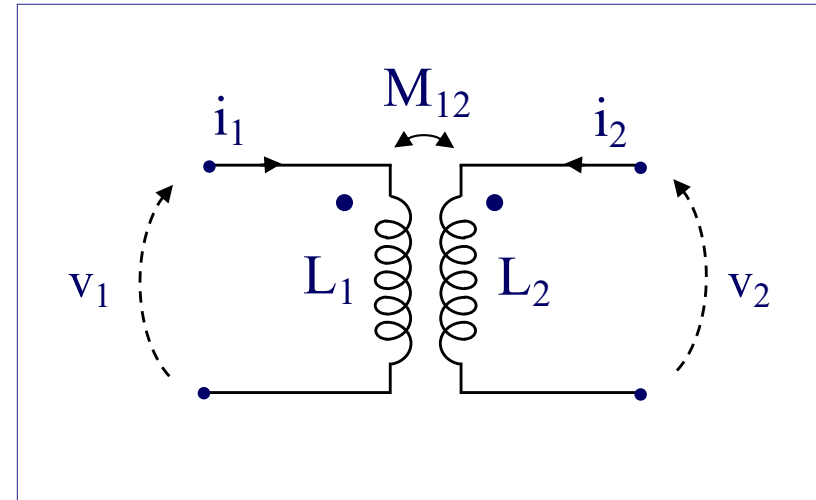




# Linear Time Independent Coupled Inductors

## Magnetic Energy Stored in Coupled Inductors

The magnetic energy  $e(-\infty, t)$  stored in the magnetically coupled inductors in the time interval from  $-\infty$  to  $t$ , assuming that  $i_1(-\infty) = i_2(-\infty) = 0$ ,  $i_1(t) = I_1$  and  $i_2(t) = I_2$ , is given by:



$$\begin{aligned}\mathcal{E}(-\infty, t) &= \int_{-\infty}^t [i_1 v_1 + i_2 v_2] dt = \\ &= \int_{-\infty}^t \left\{ L_1 i_1 \frac{di_1}{dt} + M_{12} \left[ i_1 \frac{di_2}{dt} + i_2 \frac{di_1}{dt} \right] + L_2 i_2 \frac{di_2}{dt} \right\} dt \\ &= \int_{-\infty}^t d \left[ \frac{1}{2} L_1 i_1^2 + M_{12} i_1 i_2 + \frac{1}{2} L_2 i_2^2 \right] \longrightarrow \\ \mathcal{E}(-\infty, t) &= \frac{1}{2} L_1 I_1^2 + M_{12} I_1 I_2 + \frac{1}{2} L_2 I_2^2\end{aligned}$$

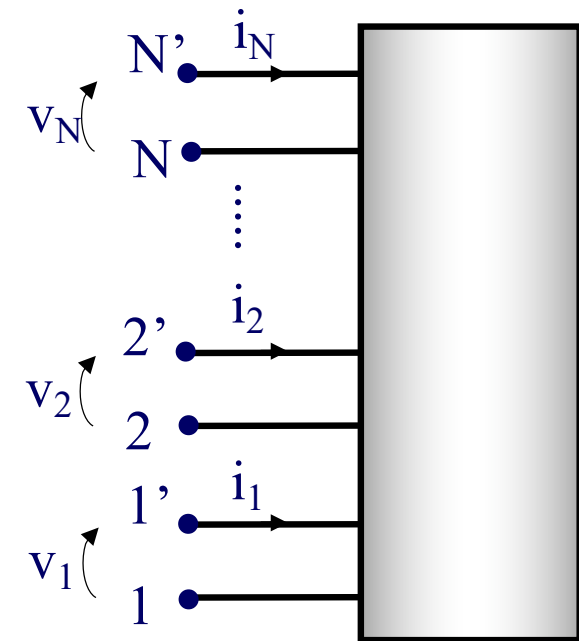


# N-Port Elements

□ In a p-terminal circuit element with p even, the terminals can be organized in pairs. When the current flowing inside the first terminal of each pair is equal to the current flowing outside the second terminal of it, the element is an N-port element with  $N = p/2$ .

□ The quantities defining a N-port element are:

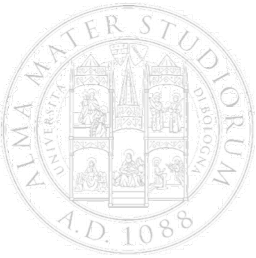
$$\mathbf{i} = \begin{bmatrix} i_1 \\ i_2 \\ \vdots \\ i_N \end{bmatrix}; \quad \mathbf{v} = \begin{bmatrix} V_1 \\ V_2 \\ \vdots \\ V_N \end{bmatrix}$$



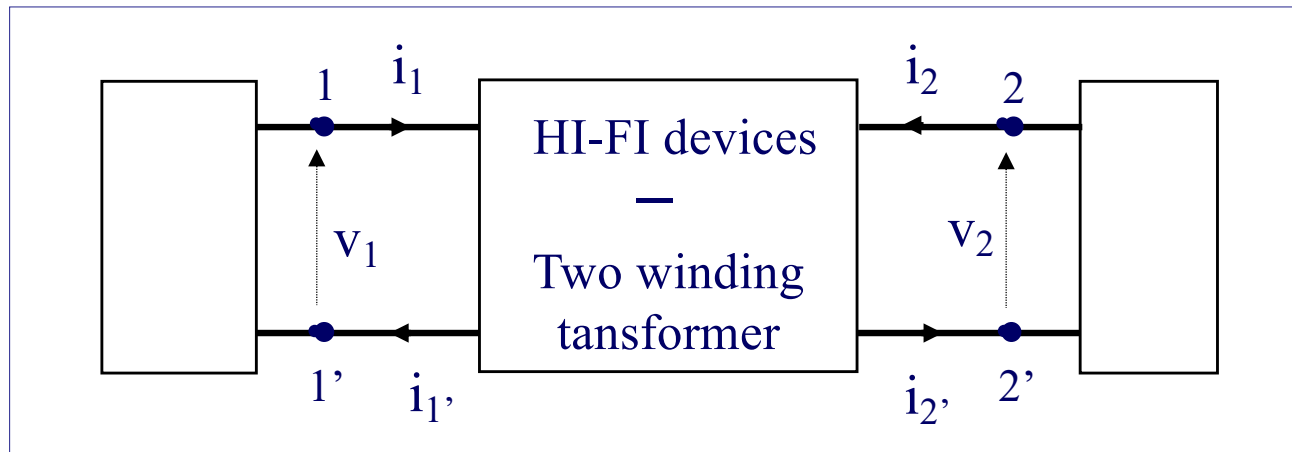
$$p(t) = v_1 i_1 + v_2 i_2 + \dots + v_N i_N = \mathbf{v}^T \mathbf{i}$$

$$\mathcal{E} = \int_{-\infty}^t p(t') dt' \quad \text{When } \mathcal{E} \geq 0 \text{ the N-port element is passive.}$$

□ The N-port element equations are N equations:  $v_k = f(i_1, i_2, i_3, \dots, i_N)$   
where:  $k = 1, 2, 3, \dots, N$ .



# Two Port Elements



- **Input port:** port 11'
- **Output port:** port 22'
- **Port tension:**  $V_1, V_2$
- **Port current:**  $i_1, i_2$  ( $i_1 = i_{1'}$ ,  $i_2 = i_{2'}$ )
- **Linear two port element**

$$\mathbf{i} = \begin{bmatrix} i_1 \\ i_2 \end{bmatrix}; \quad \mathbf{v} = \begin{bmatrix} V_1 \\ V_2 \end{bmatrix}$$

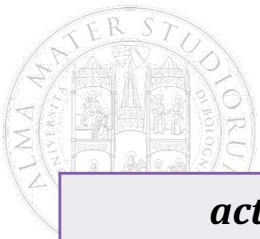
$$\mathbf{A} \mathbf{v} + \mathbf{B} \mathbf{i} + \mathbf{c} = \mathbf{0}$$

$$p = \mathbf{v}^T \mathbf{i}$$

$$a_{11}v_1 + a_{12}v_2 + b_{11}i_1 + b_{12}i_2 + c_1 = 0$$

$$a_{12}v_1 + a_{22}v_2 + b_{21}i_1 + b_{22}i_2 + c_2 = 0$$

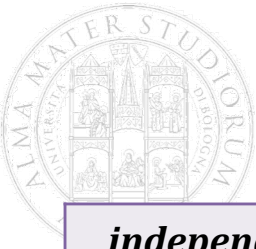
$$p = v_1 i_1 + v_2 i_2$$



# Terminology: English – Chinese

<i>active element</i>	有源元件
<i>branch</i>	支路
<i>branch current</i>	支路电流
<i>branch tension</i>	支路电压
<i>capacitor</i>	电容器
<i>capacitance</i>	容量
<i>circuit component</i>	电路元件
<i>circuit element</i>	电路元件
<i>closed circuit</i>	闭合电路
<i>conductance</i>	电导
<i>conducting plates, armatures</i>	电导板, 电枢
<i>conductivity</i>	电导率
<i>controlled source</i>	受控源
<i>current controlled element</i>	电流控制元件
<i>current divider</i>	分流

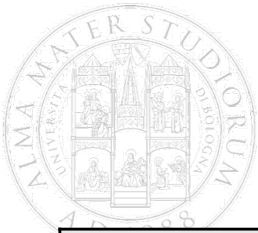
<i>coupled inductors</i>	耦合电感
<i>delta connection</i>	三角形连接
<i>dielectric constant</i>	介电常数
<i>dielectric, insulator</i>	电介质, 绝缘体
<i>diode</i>	二极管
<i>dissipative element</i>	耗散元件
<i>distributed parameters</i>	分布参数
<i>electric circuit</i>	电路
<i>electric power</i>	电力
<i>electric resistance</i>	电阻
<i>electromagnetic energy</i>	电磁能
<i>electrostatic energy</i>	静电能
<i>element equation</i>	元件方程
<i>independent current source</i>	独立电流源



# Terminology: English – Chinese

<b><i>independent voltage source</i></b>	独立电压源
<b><i>inductance</i></b>	电感
<b><i>inductor</i></b>	电感
<b><i>linear (non-linear) element</i></b>	线性(非线性)元件
<b><i>lumped parameters</i></b>	集总参数
<b><i>Kirchhoff's current law</i></b>	基尔霍夫电流定律
<b><i>Kirchhoff's tension law</i></b>	基尔霍夫电压定律
<b><i>monolithic chip</i></b>	单芯片
<b><i>mutual inductance</i></b>	互感
<b><i>n-terminal element</i></b>	N端口元件
<b><i>n-port element</i></b>	N端口元件
<b><i>two-port element</i></b>	两端元件
<b><i>node</i></b>	节点

<b><i>open circuit</i></b>	开路
<b><i>parallel connection</i></b>	并联
<b><i>passive element</i></b>	被动元件
<b><i>pn-junction</i></b>	Pn结
<b><i>resistor</i></b>	电阻
<b><i>resistance</i></b>	电阻
<b><i>resistivity</i></b>	电阻率
<b><i>self inductance</i></b>	自感
<b><i>series connection</i></b>	串联
<b><i>storage element</i></b>	储能元件
<b><i>tension divider</i></b>	分压
<b><i>two terminal circuit element</i></b>	两端电路元件
<b><i>voltage controlled element</i></b>	电压控制元件
<b><i>wye connection</i></b>	星形连结



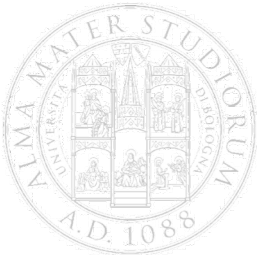
# Terminology

## English – Italian

<i>active element</i>	<i>elemento attivo</i>
<i>branch</i>	<i>ramo, lato</i>
<i>branch current</i>	<i>corrente di ramo</i>
<i>branch tension</i>	<i>tensione di ramo</i>
<i>capacitor</i>	<i>condensatore</i>
<i>capacitance</i>	<i>capacità</i>
<i>circuit component</i>	<i>componente circuitale</i>
<i>circuit element</i>	<i>elemento circuitale</i>
<i>closed circuit</i>	<i>circuito chiuso</i>
<i>conductance</i>	<i>conduttanza</i>
<i>conducting plates, armatures</i>	<i>armature di condensatore</i>
<i>conductivity</i>	<i>Conducibilità</i>
<i>controlled source</i>	<i>generatore controllato</i>
<i>current controlled element</i>	<i>elemento controllato in corrente</i>
<i>current divider</i>	<i>partitore di corrente</i>

<i>coupled inductors</i>	<i>induttori accoppiati</i>
<i>delta connection</i>	<i>connessione a triangolo</i>
<i>dielectric constant</i>	<i>costante dielettrica</i>
<i>dielectric, insulator</i>	<i>dielettrico, isolante</i>
<i>diode</i>	<i>diodo</i>
<i>dissipative element</i>	<i>elemento dissipativo</i>
<i>distributed parameters</i>	<i>parametri distribuiti</i>
<i>electric circuit</i>	<i>circuito elettrico</i>
<i>electric power</i>	<i>potenza elettrica</i>
<i>electric resistance</i>	<i>resistenza elettrica</i>
<i>electromagnetic energy</i>	<i>energia elettromagnetica</i>
<i>electrostatic energy</i>	<i>energia elettrostatica</i>
<i>element equation</i>	<i>equazione caratteristica, equazione costitutiva</i>
<i>independent current source</i>	<i>generatore di current indipendente</i>





# Terminology

## English – Italian

<i>independent voltage source</i>	<i>generatore di tensione indipendente</i>
<i>inductance</i>	<i>induttanza</i>
<i>inductor</i>	<i>induttore</i>
<i>linear (non-linear) element</i>	<i>elemento lineare (non lineare)</i>
<i>lumped parameters</i>	<i>parametri concentrati</i>
<i>Kirchhoff's current law</i>	<i>legge di Kirchhoff delle correnti</i>
<i>Kirchhoff's tension law</i>	<i>legge di Kirchhoff delle tensioni</i>
<i>monolithic chip</i>	<i>chip monolitico</i>
<i>mutual inductance</i>	<i>mutua induttanza</i>
<i>n-terminal element</i>	<i>elemento ad n poli</i>
<i>n-port element</i>	<i>elemento a n porte</i>
<i>two-port element</i>	<i>elemento a due porte</i>
<i>node</i>	<i>nodo, polo</i>

<i>open circuit</i>	<i>circuito aperto</i>
<i>parallel connection</i>	<i>connessione in parallelo</i>
<i>passive element</i>	<i>elemento passivo</i>
<i>pn-junction</i>	<i>giunzione pn</i>
<i>resistor</i>	<i>resistore</i>
<i>resistance</i>	<i>resistenza</i>
<i>resistivity</i>	<i>resistività</i>
<i>self inductance</i>	<i>autoinduttanza</i>
<i>series connection</i>	<i>connessione in serie</i>
<i>storage element</i>	<i>elemento con memoria</i>
<i>tension divider</i>	<i>partitore di tensione</i>
<i>two terminal circuit element</i>	<i>elemento circuitale a due terminali, bopolo</i>
<i>voltage controlled element</i>	<i>elemento controllato in tensione</i>
<i>wye connection</i>	<i>connessione a stella</i>